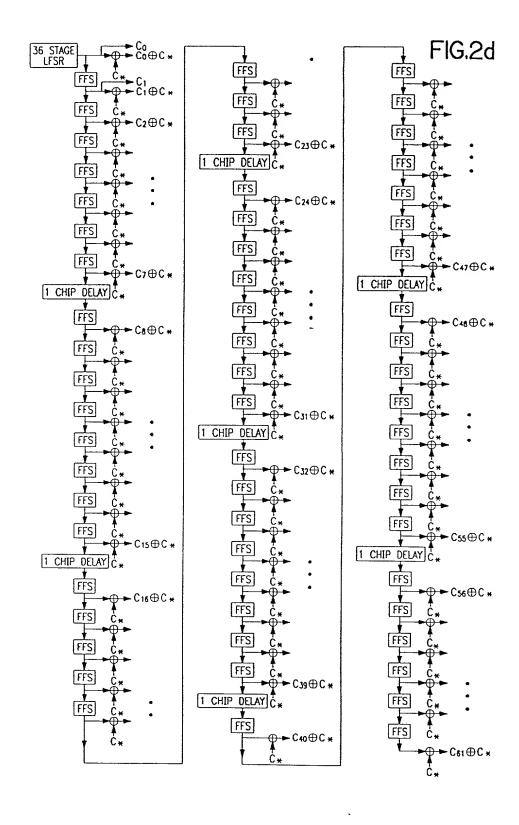


222

CODE MEMORY



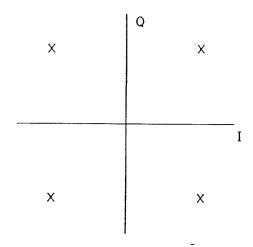


FIG. 3a

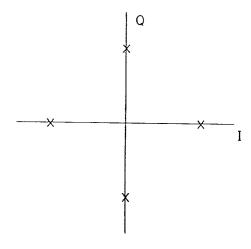
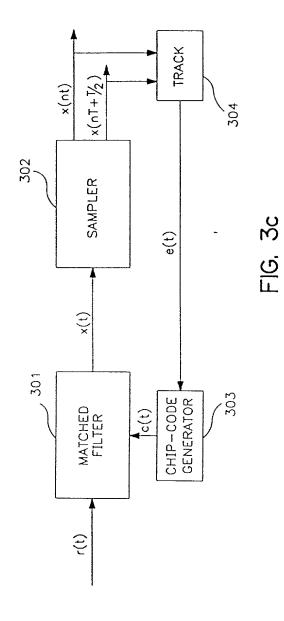
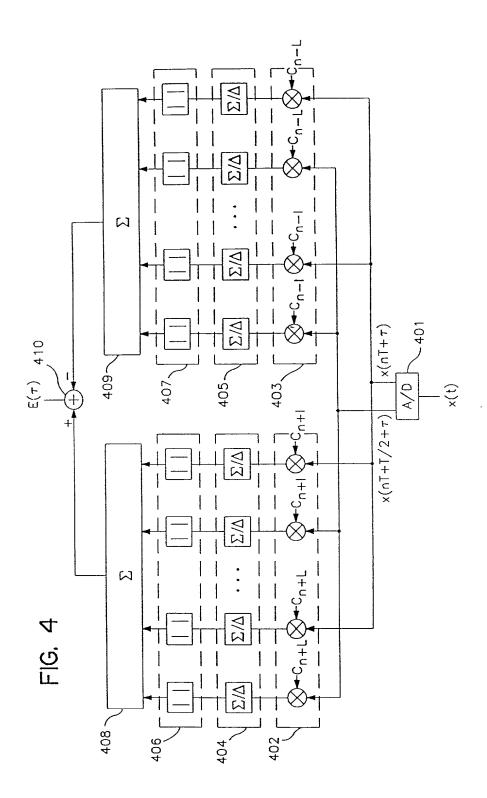


FIG. 3b





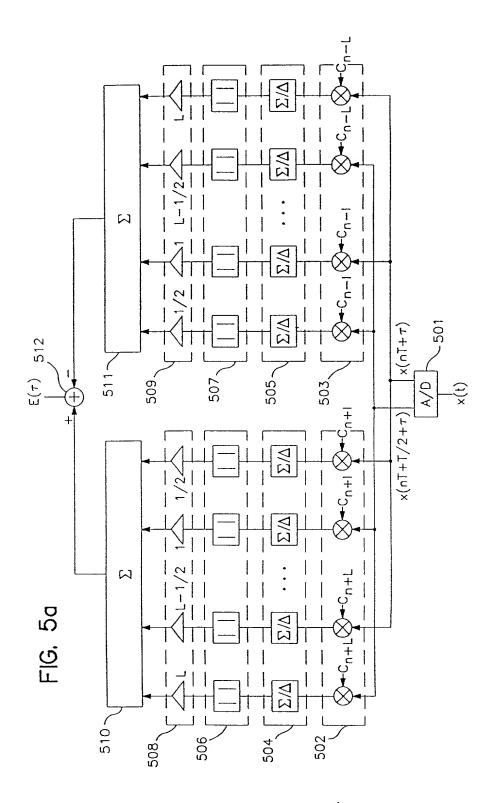
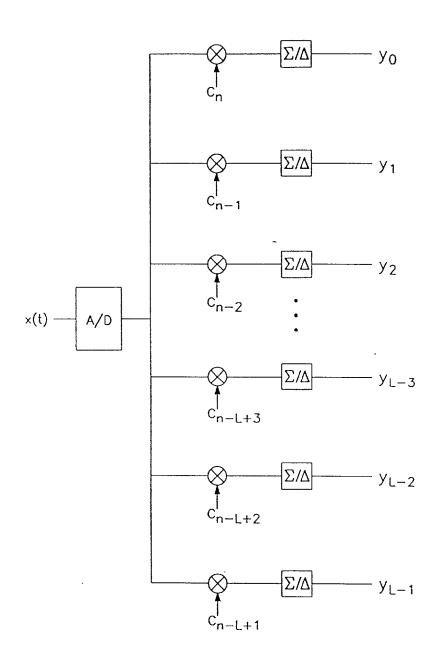


FIG. 5b



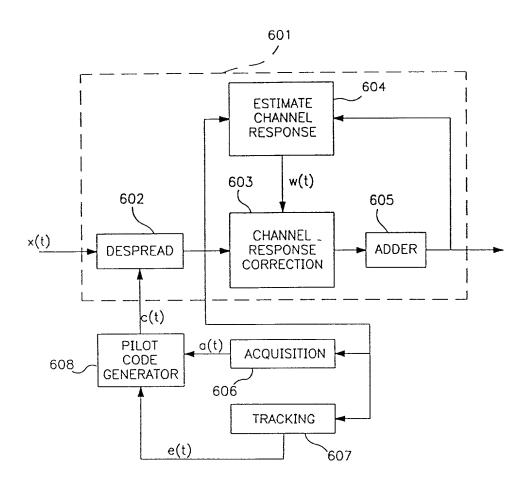
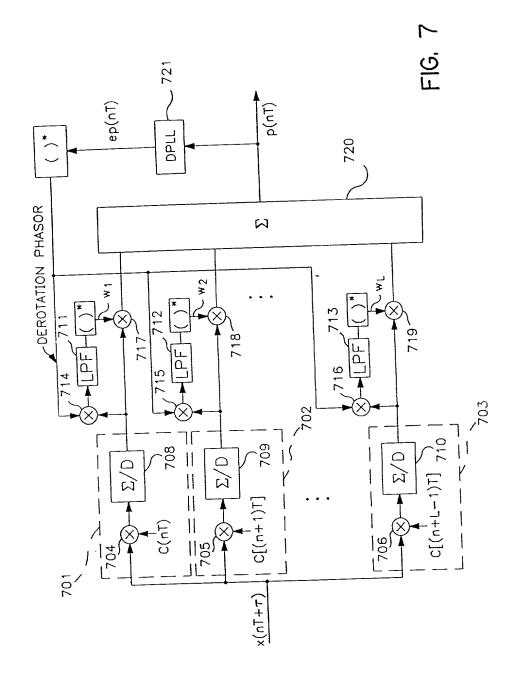


FIG. 6



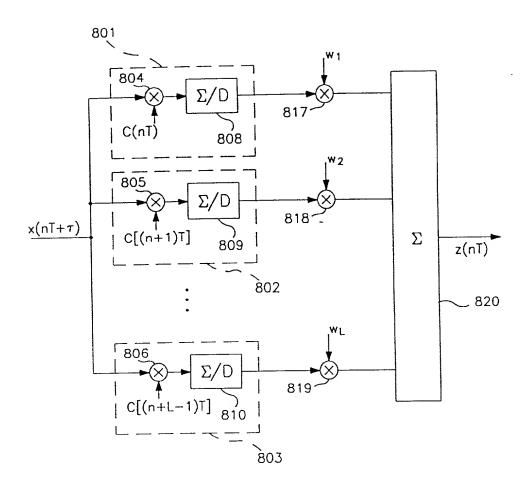


FIG. 8a

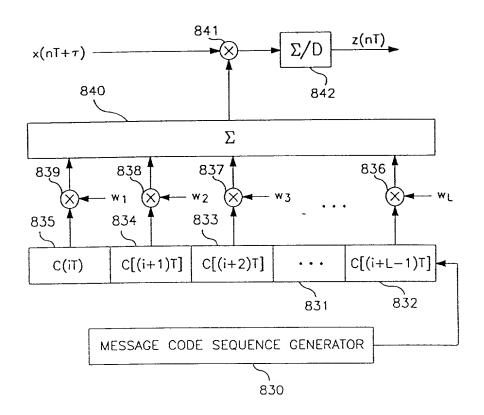


FIG. 8b

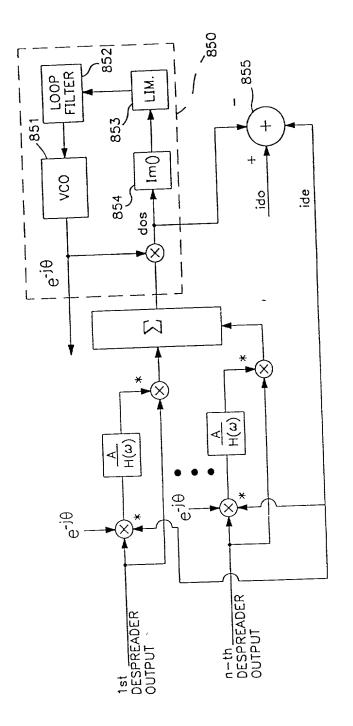
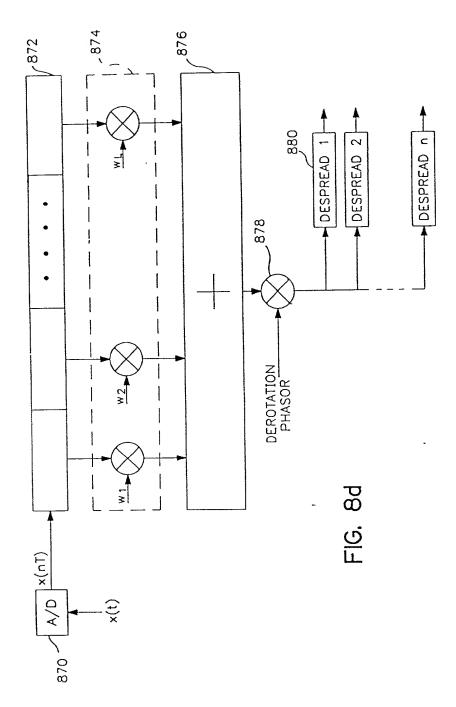
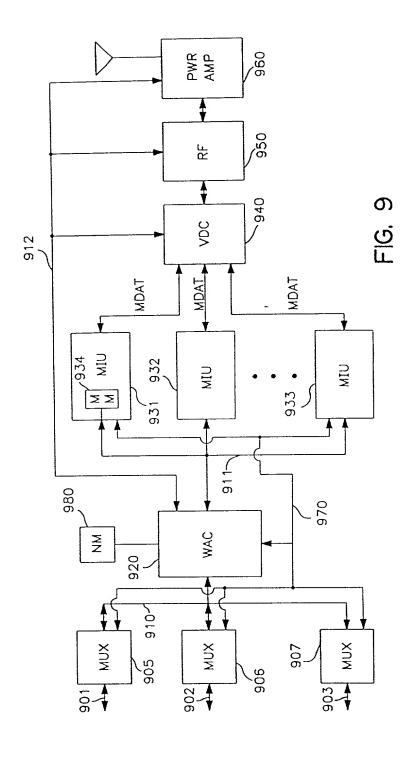
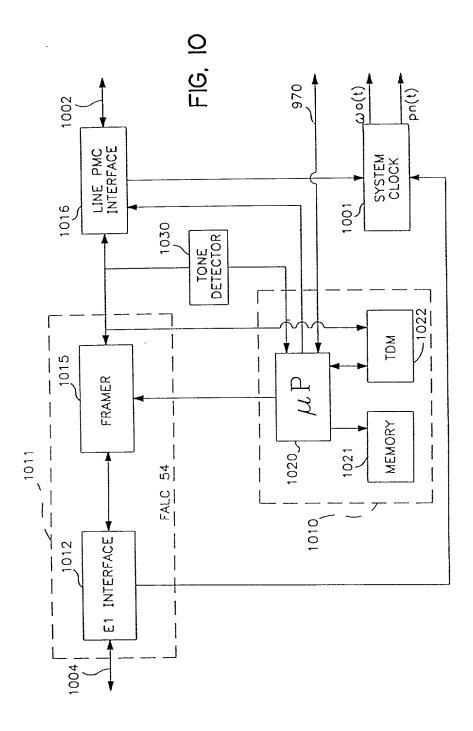
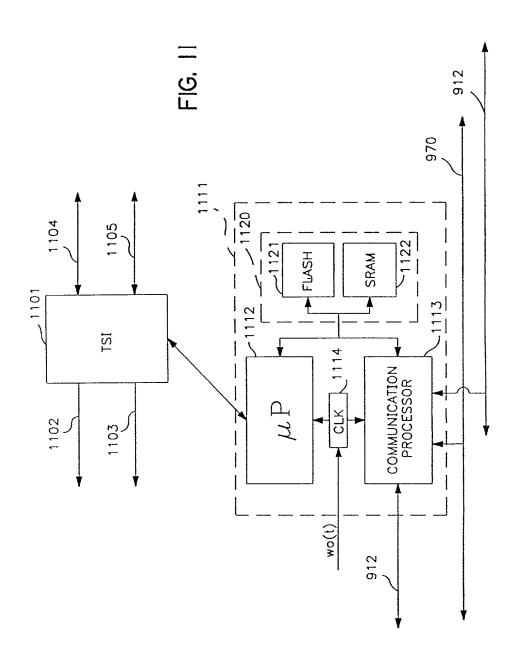


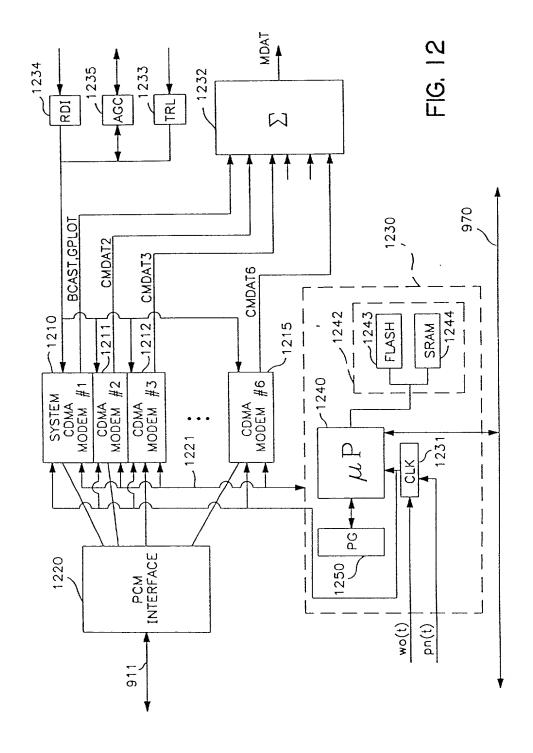
FIG. 8c











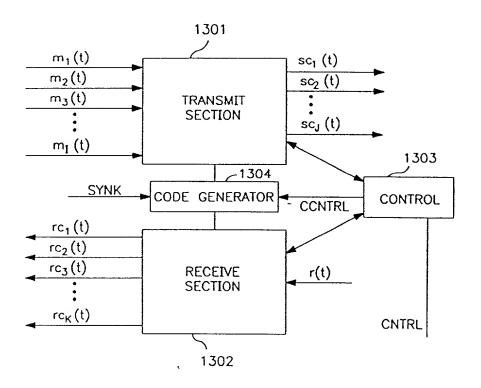
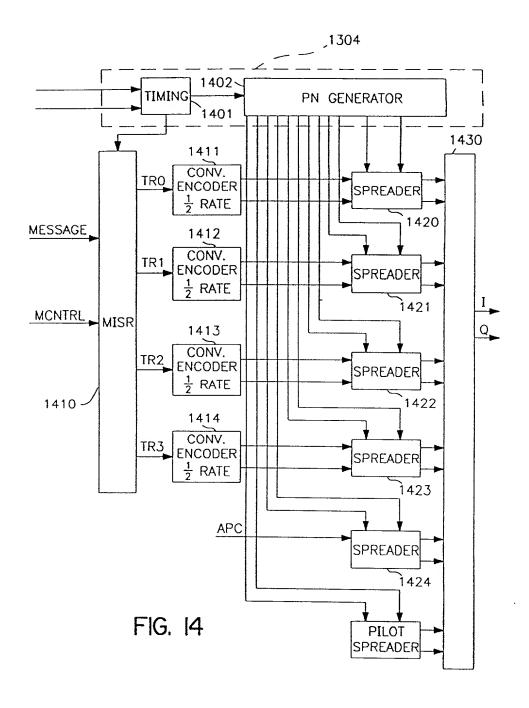
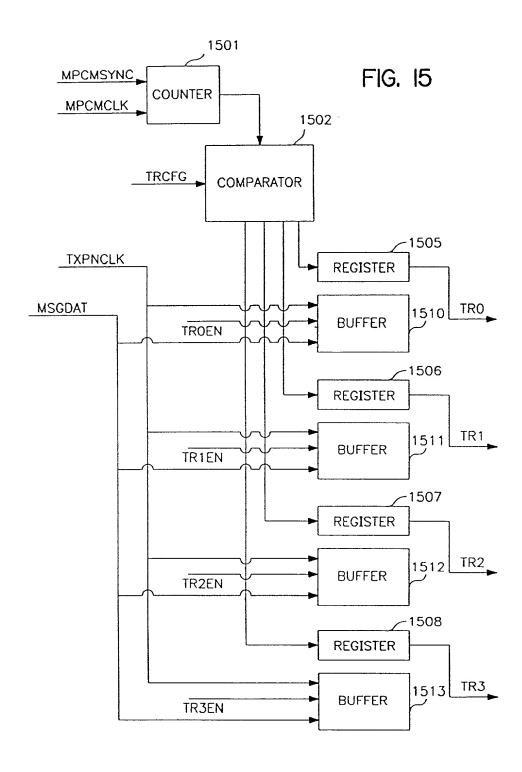


FIG. 13





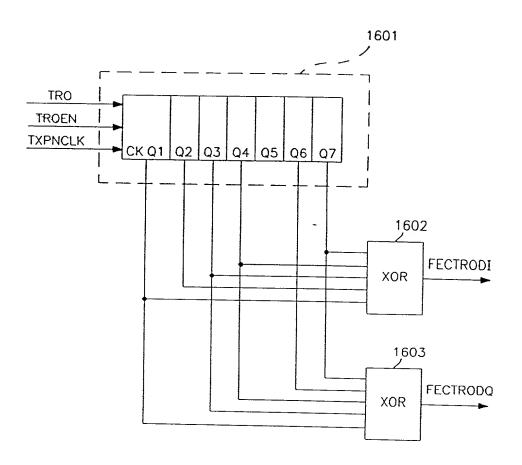


FIG. 16

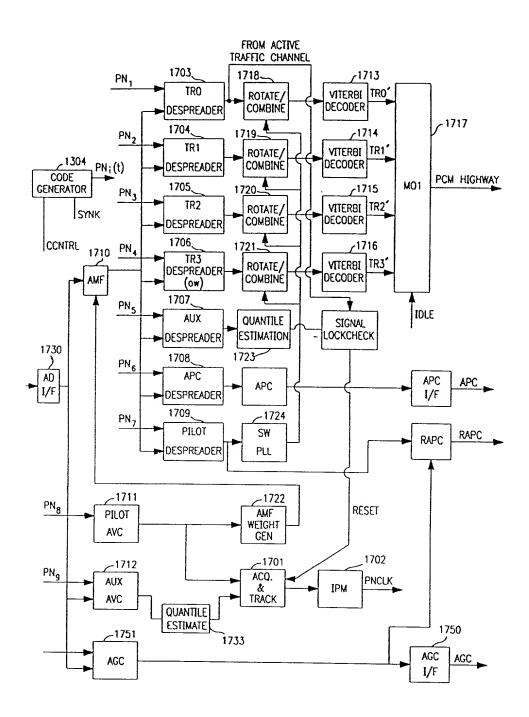
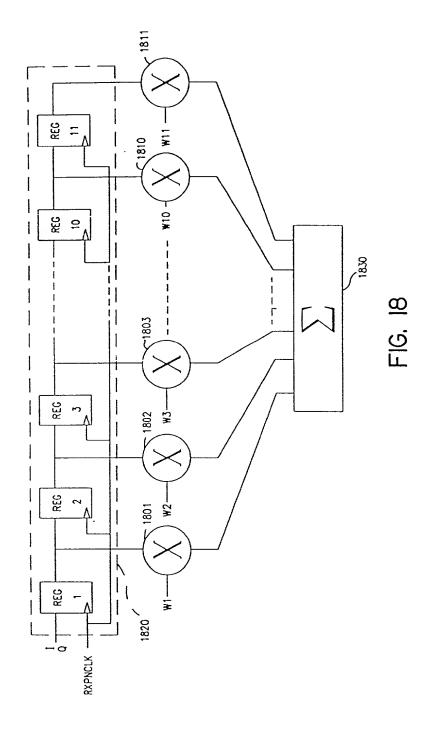
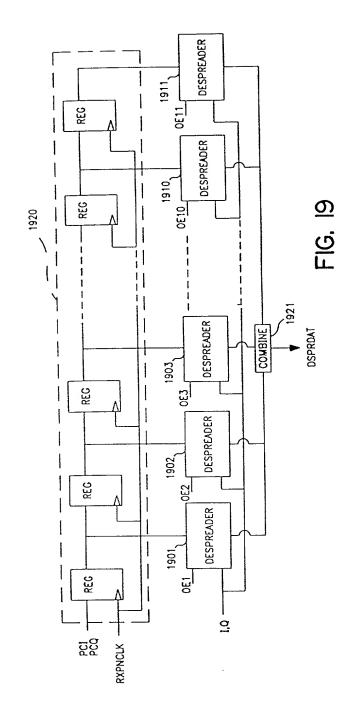
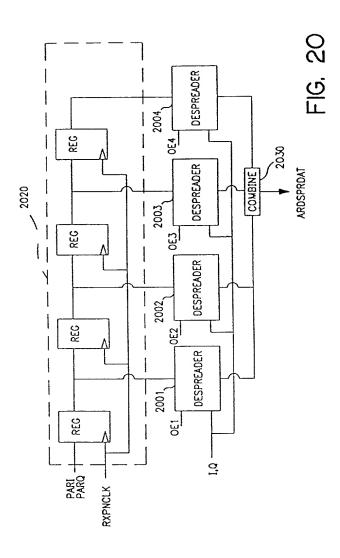
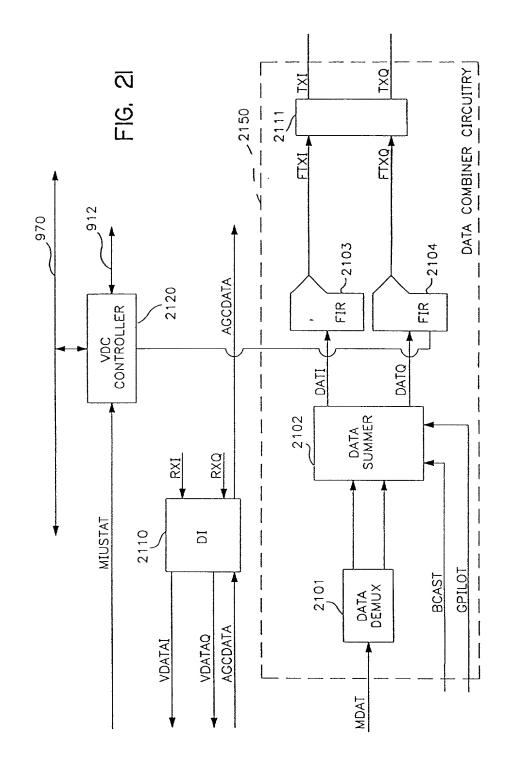


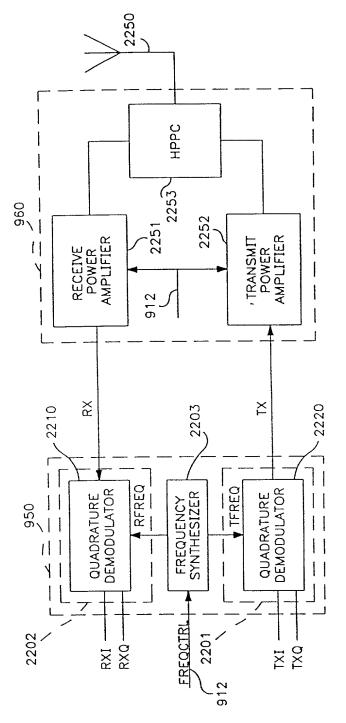
FIG. 17



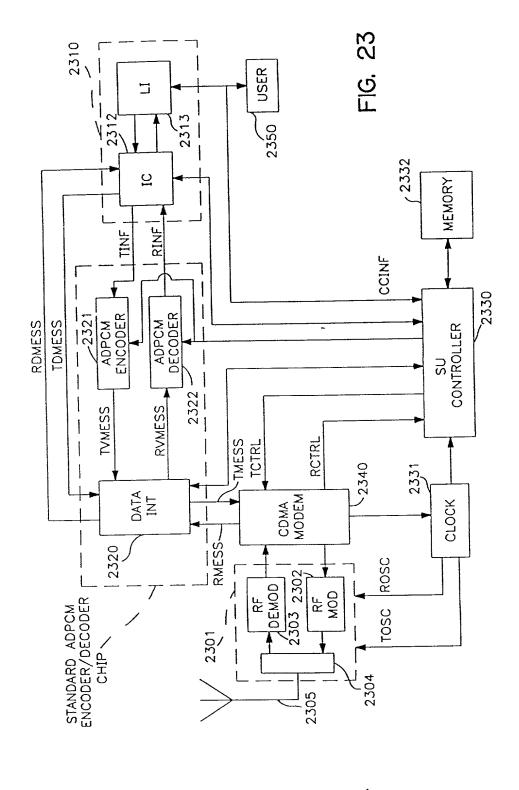








FIG, 22



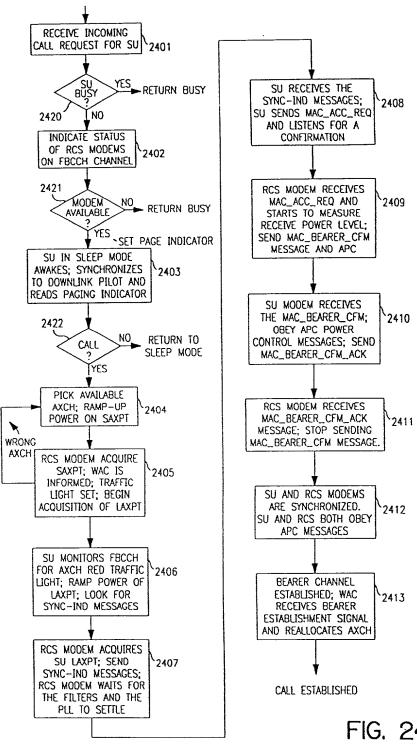
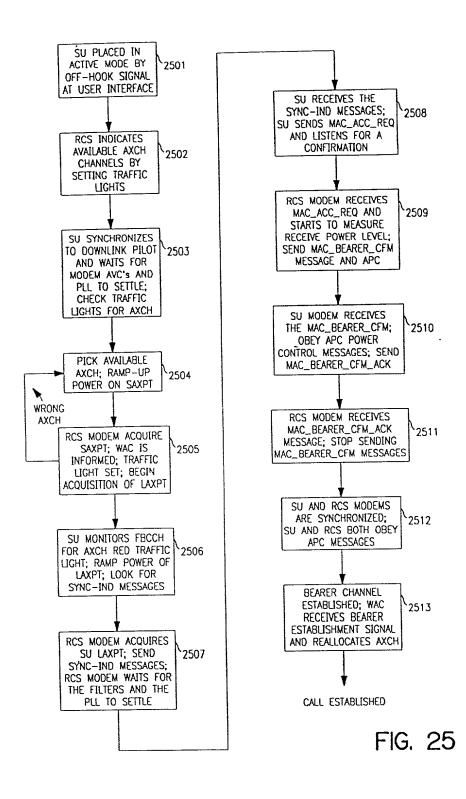


FIG. 24



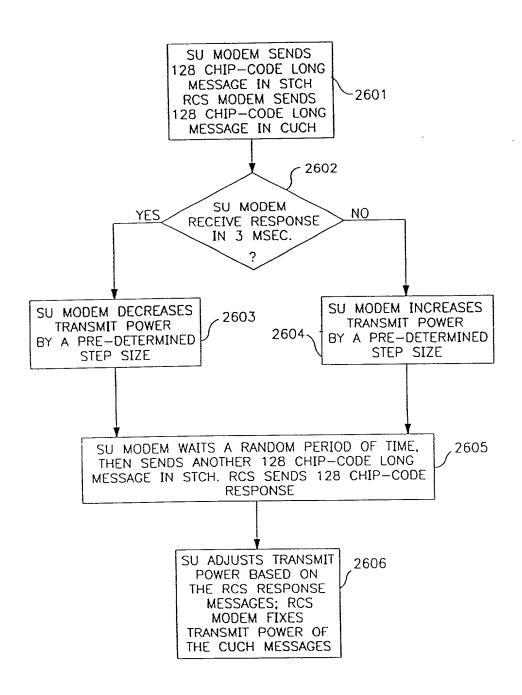


FIG. 26

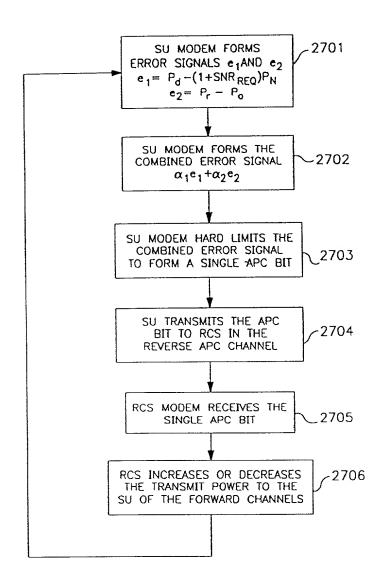


FIG. 27

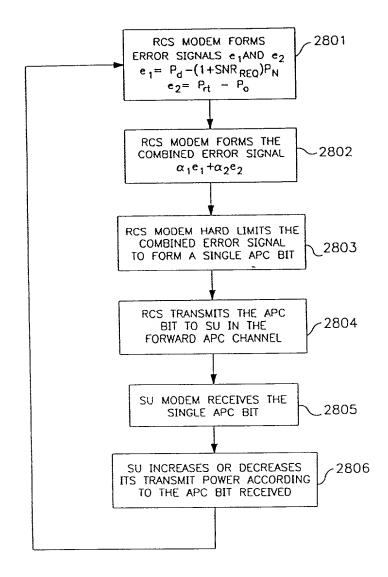
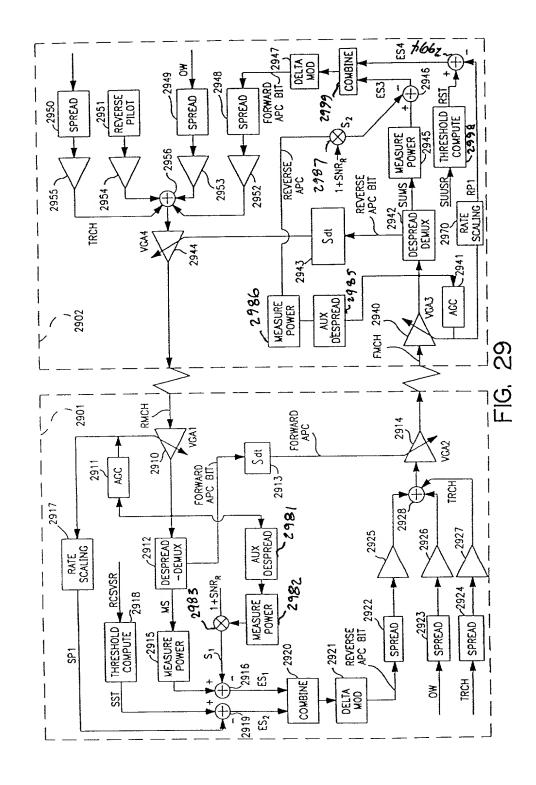


FIG. 28



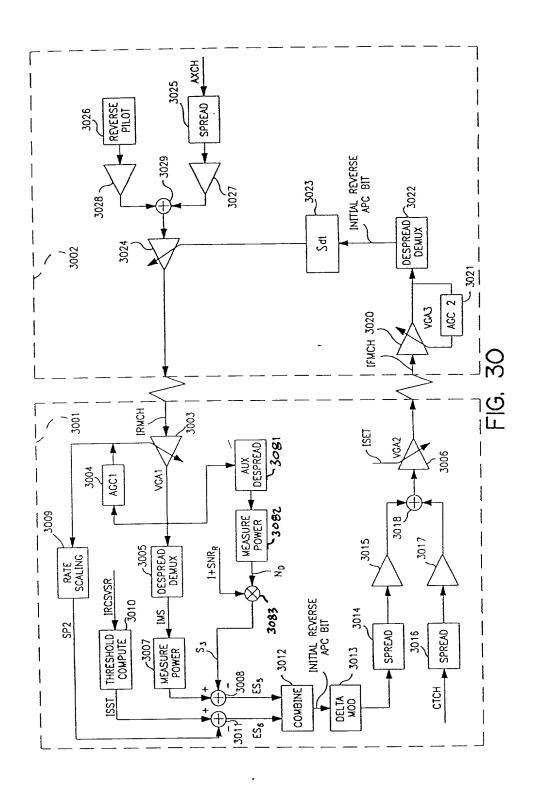


FIG.31

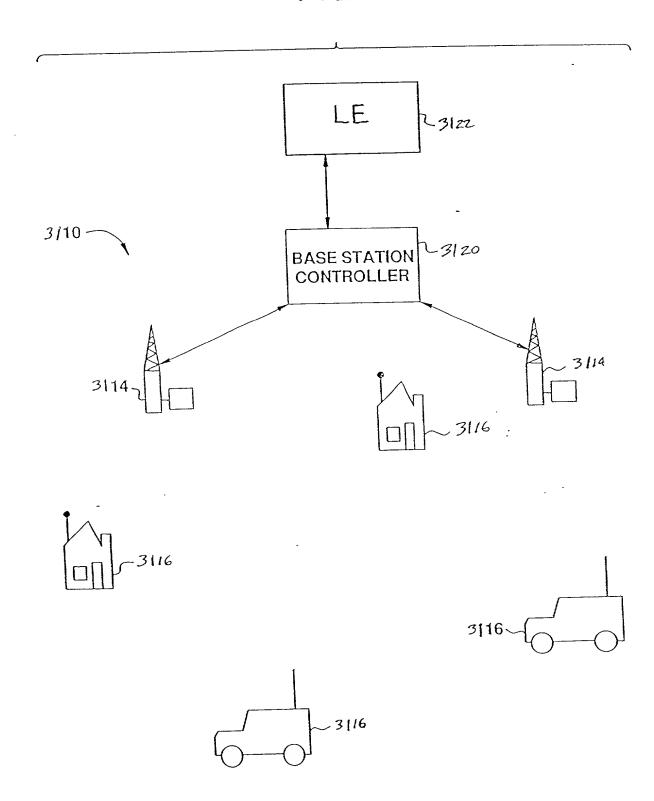
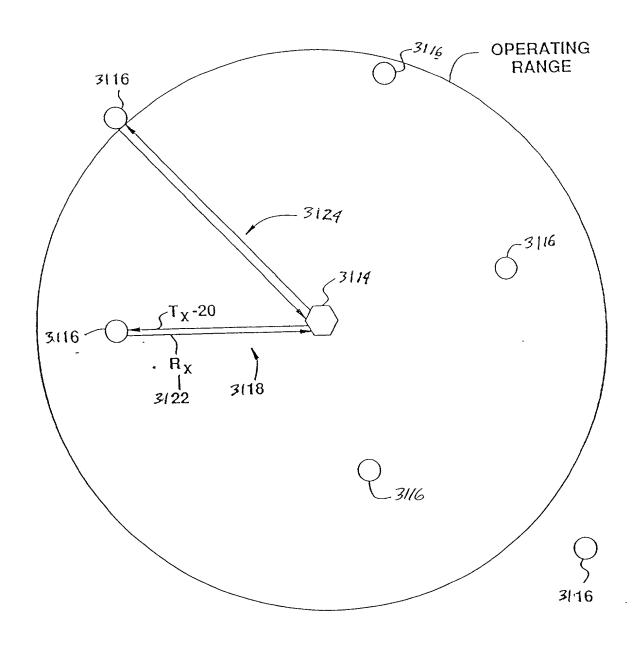


FIG.32



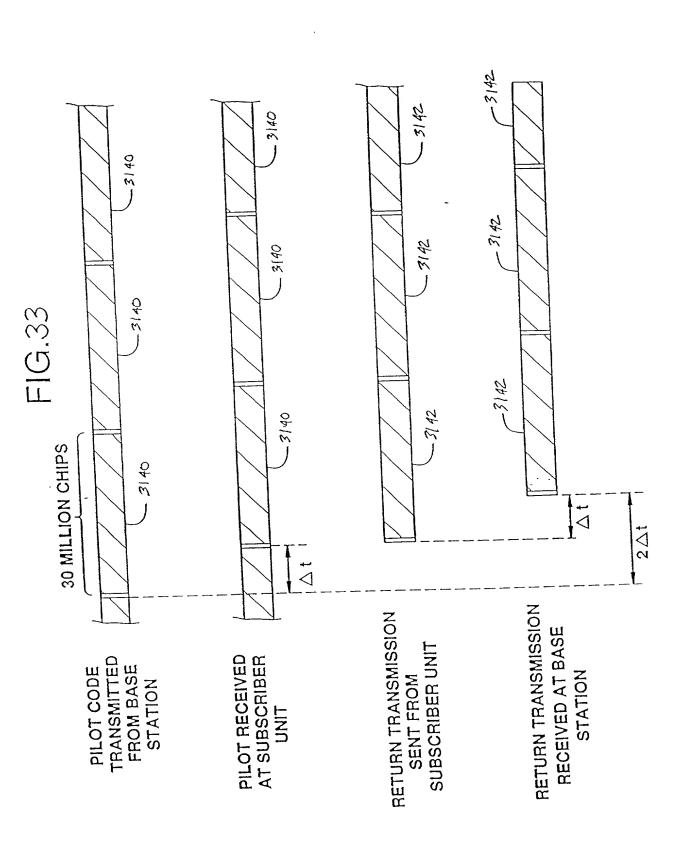
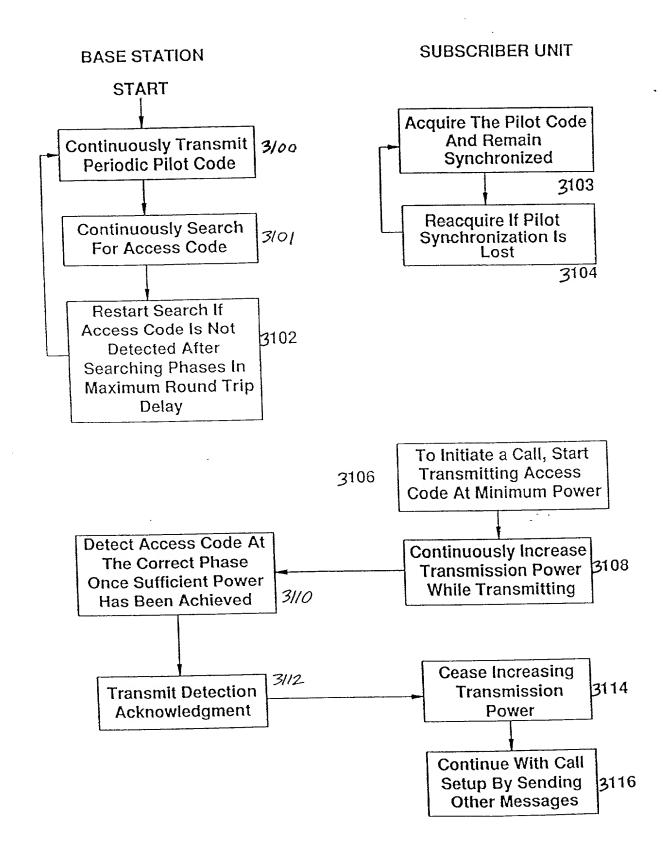


FIG.34



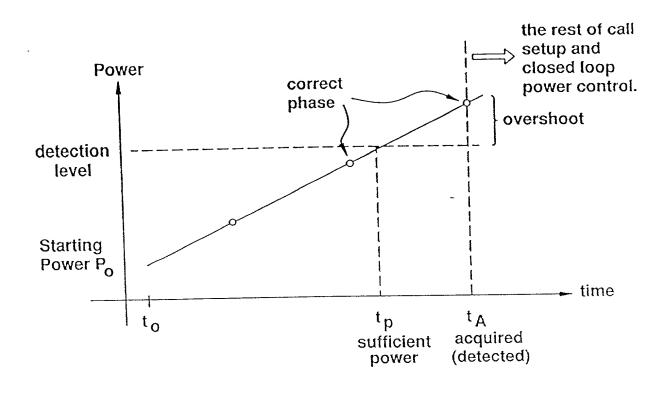


FIG.37

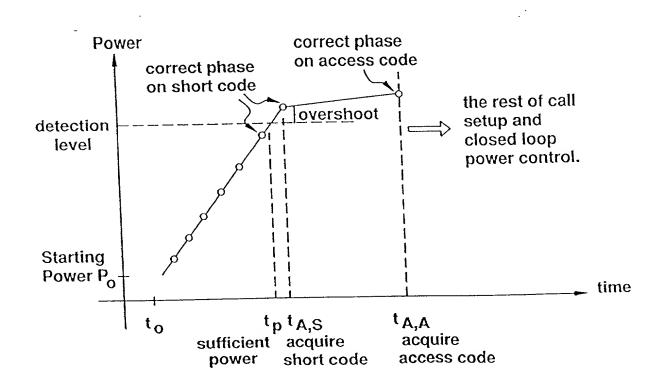


FIG.36A

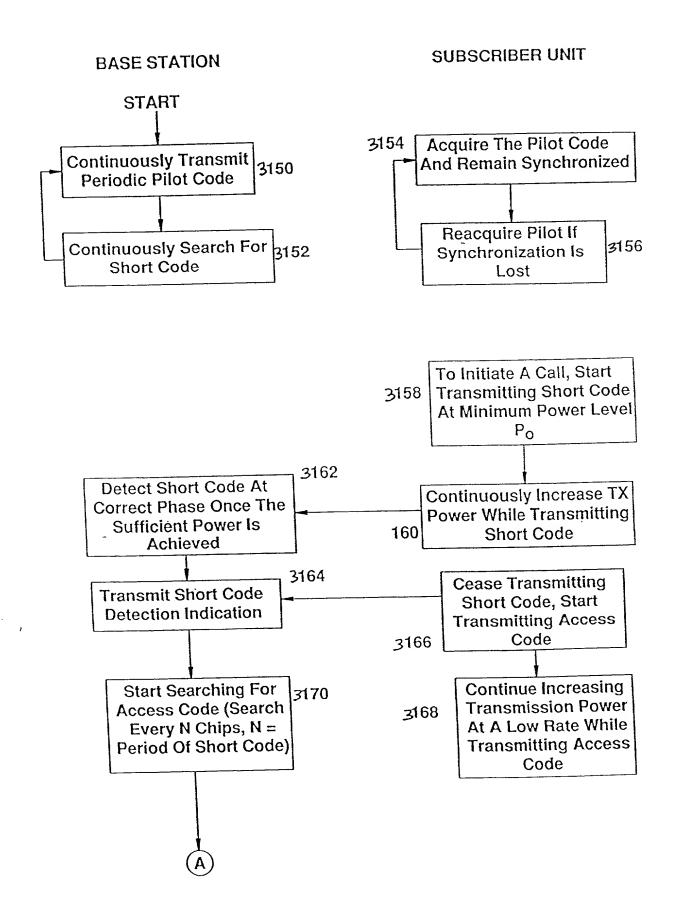


FIG.36B

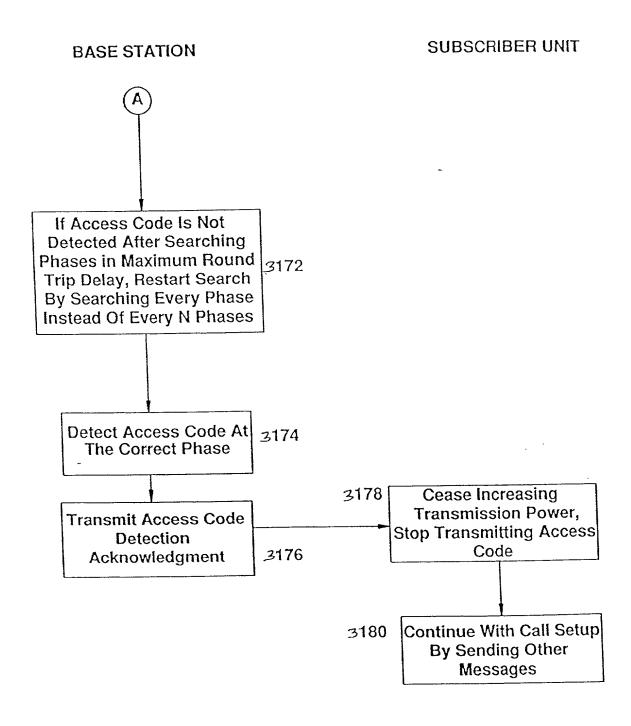
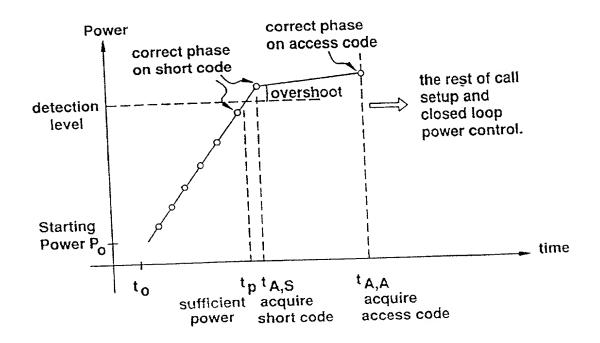
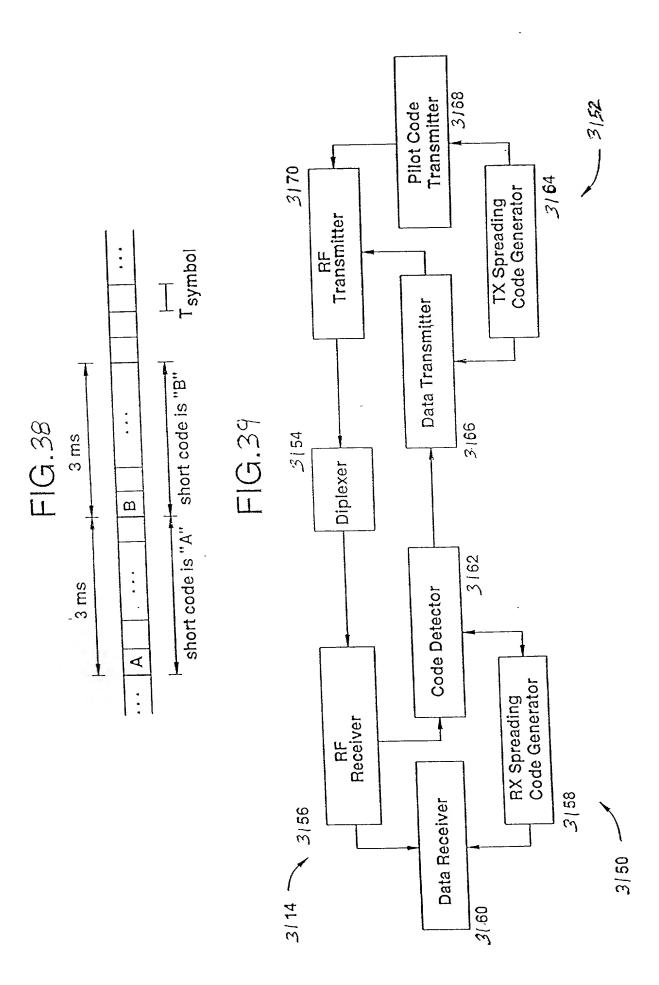


FIG.37





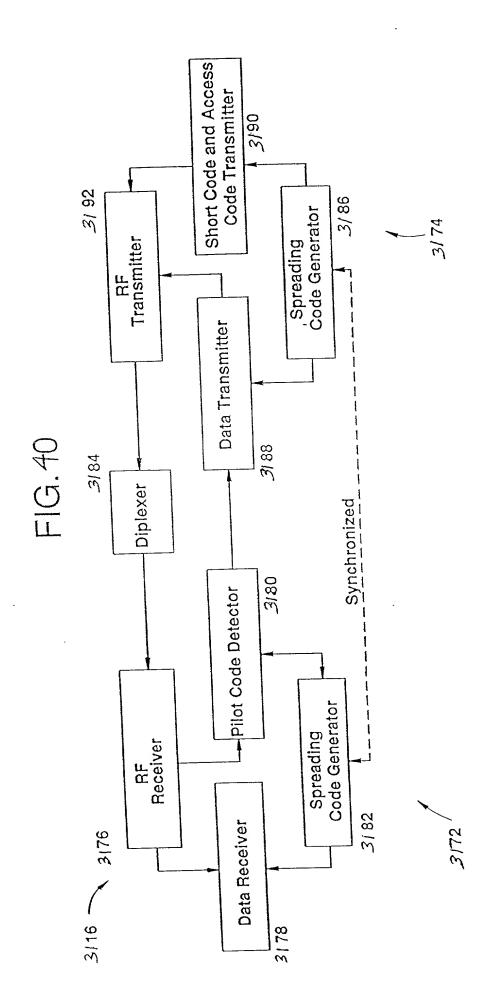


FIG.41A

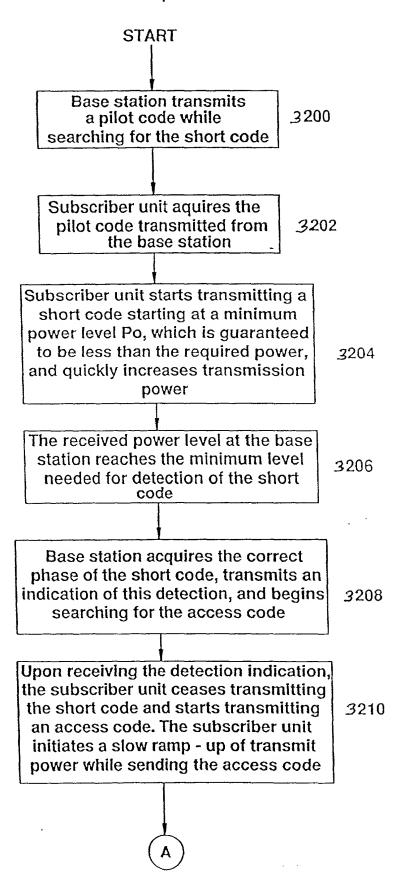


FIG.41B



Base station searches for the correct phase of the acces code by searching only one phase out of each short code length portion of the access code

3212

If the base station searches the phases of the access code up to the maximum round trip delay and has not detected the correct phase, repeat search by searching every phase

3214

Upon detection of the correct phase of the access code by the base station, the base station sends an acknowledgement to the subscriber unit

3216

Reception of the acknowledgement by the subscriber unit concludes the ramp - up process. A closed loop power control is established, and the subscriber unit continues the call setup process by sending releted call setup messages

3218

FIG. 42 (PRIOR ART)

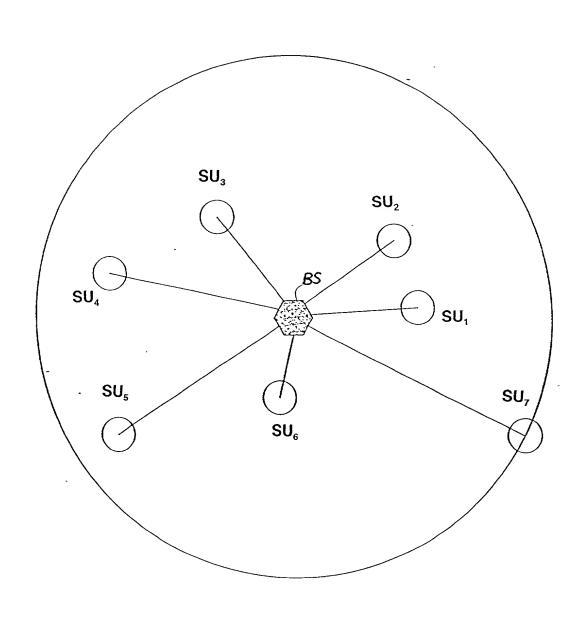
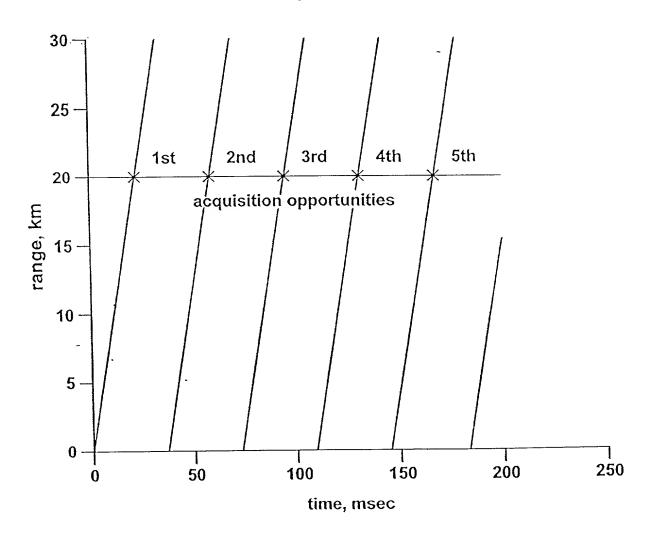


FIG. 43 (prior art)

Mean Cell Sweep Time, FSU @ 20 KM



.

FIG.44

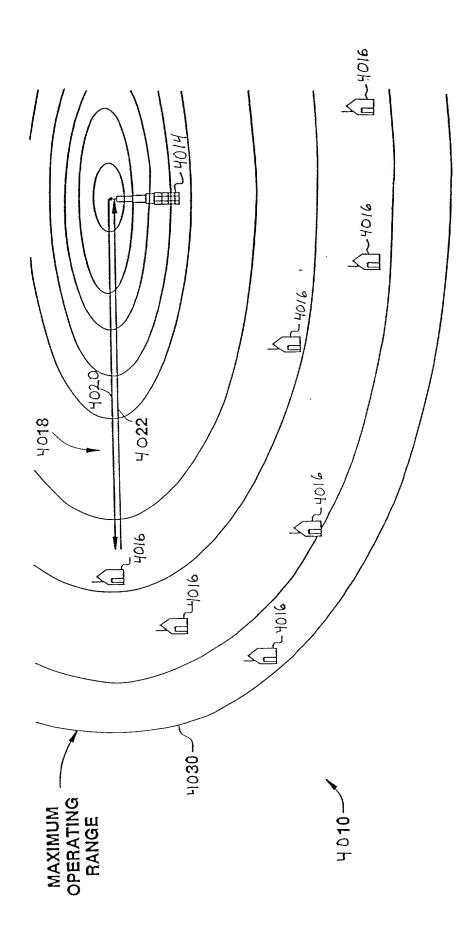


FIG. 45

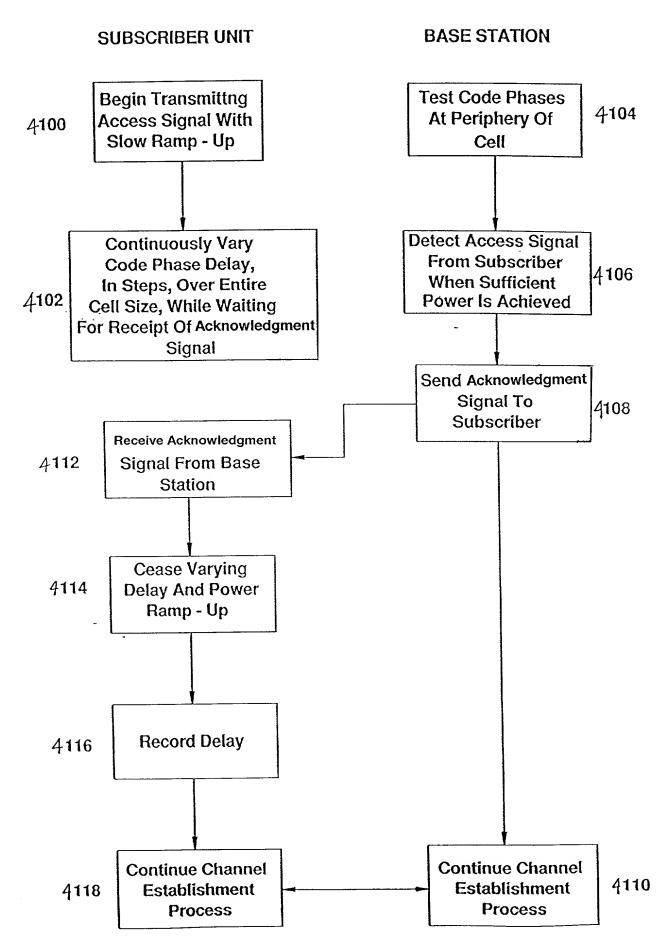
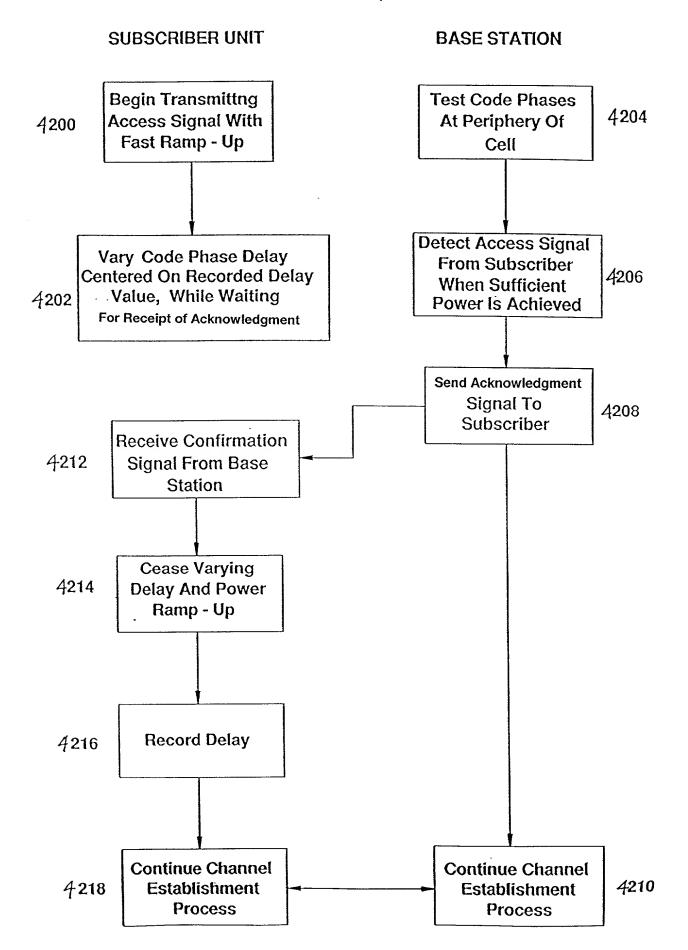
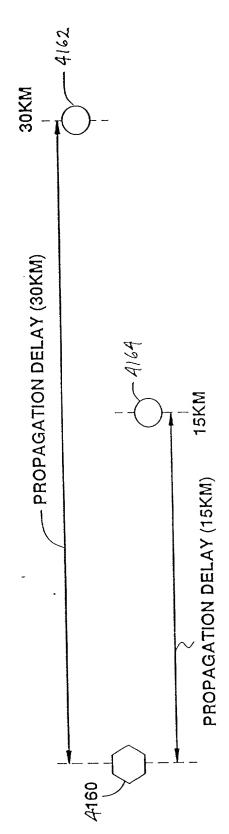
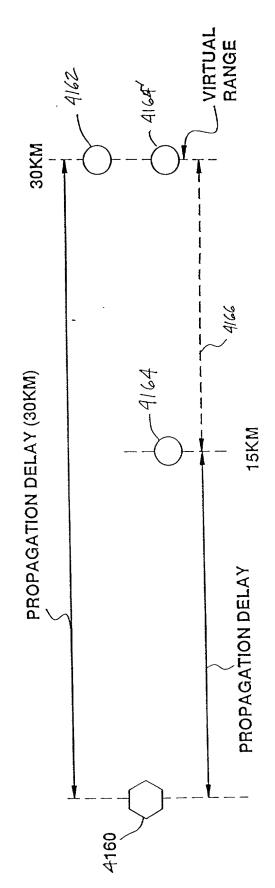


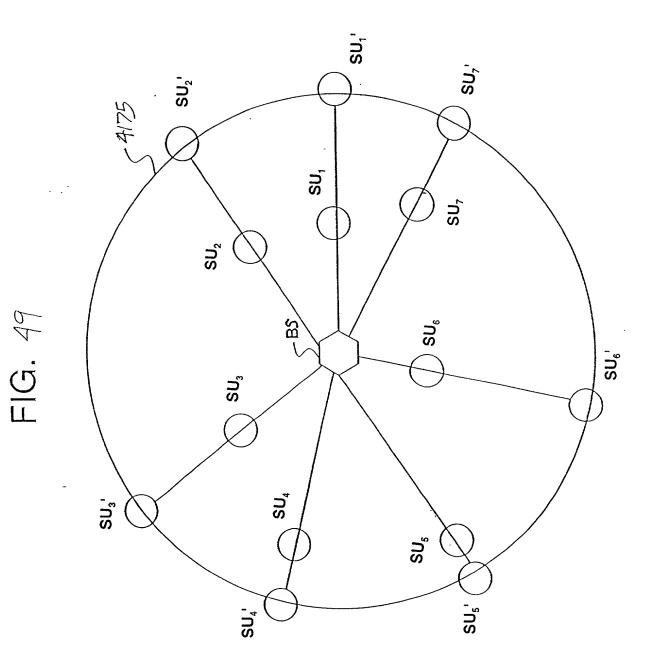
FIG. 46











4234 4232 BB AMP 4230 Band -Pass Filter ③ 5-4228 4204 4224 Low -Pass Filter 4226 4220 PN Sequence Low -Pass Filter 4216 4202 Sequence J Nd П С 4210 (1) Band -Pass Filter 4208

De pro pro productive that that world constraints then the

FIG. 51

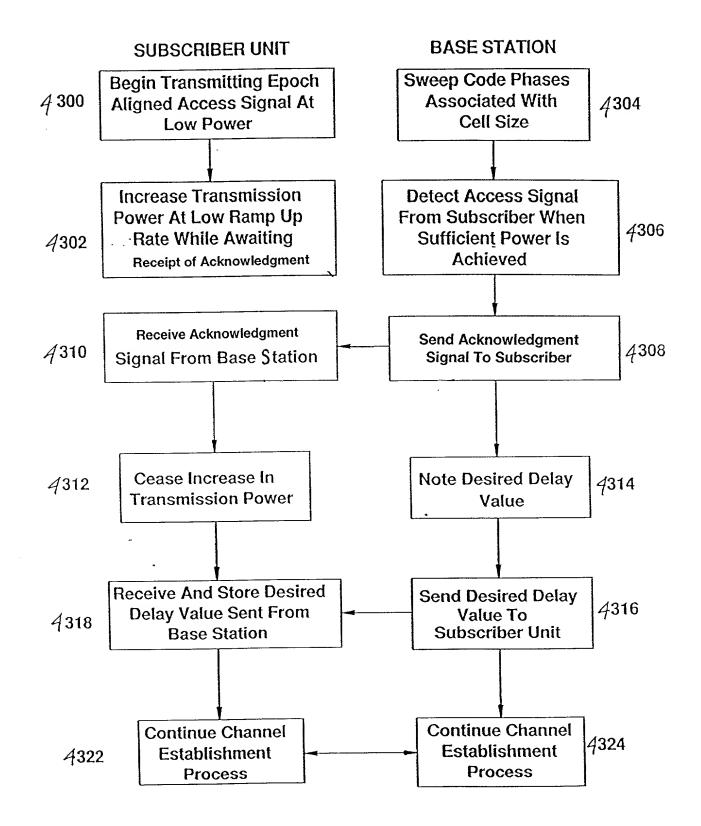
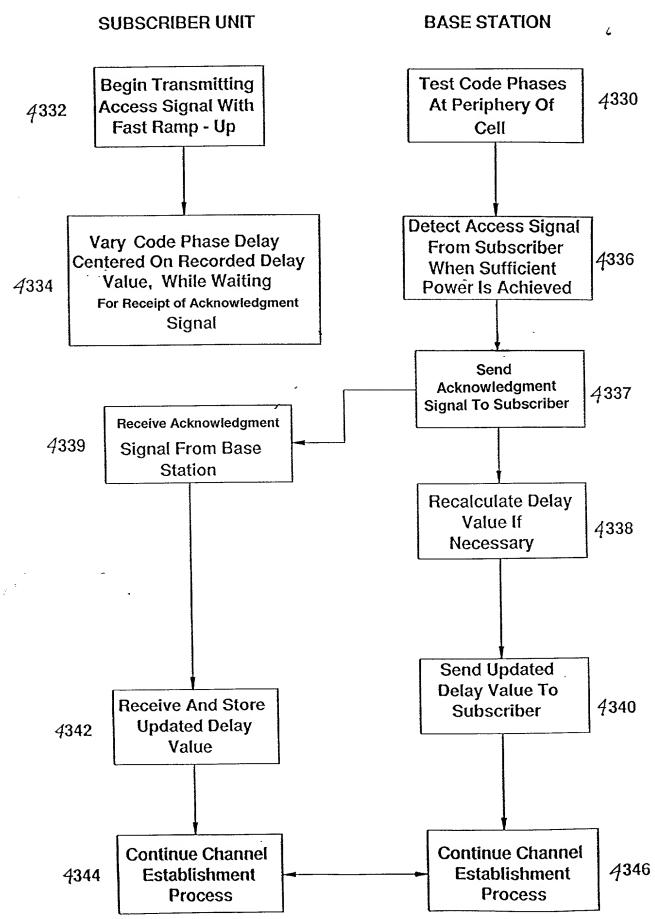
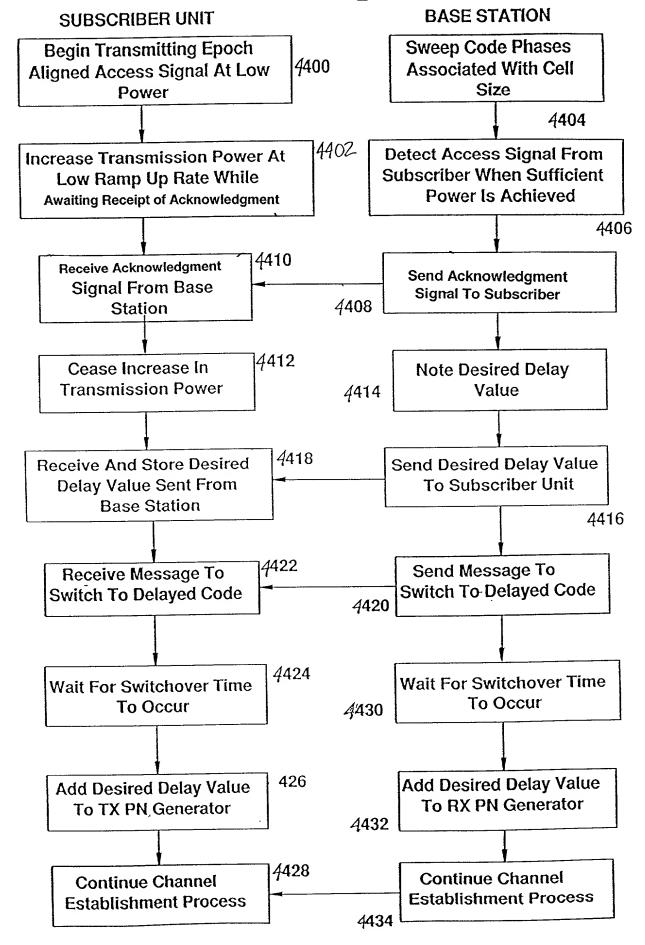
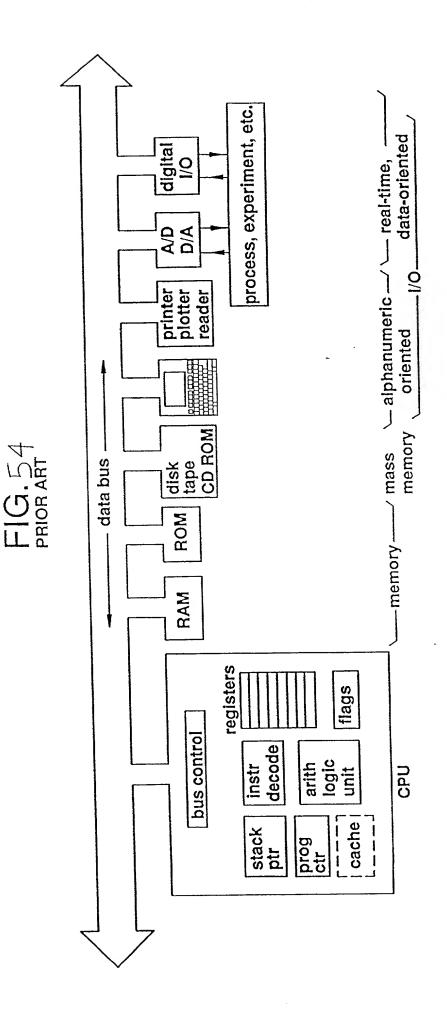


FIG.52





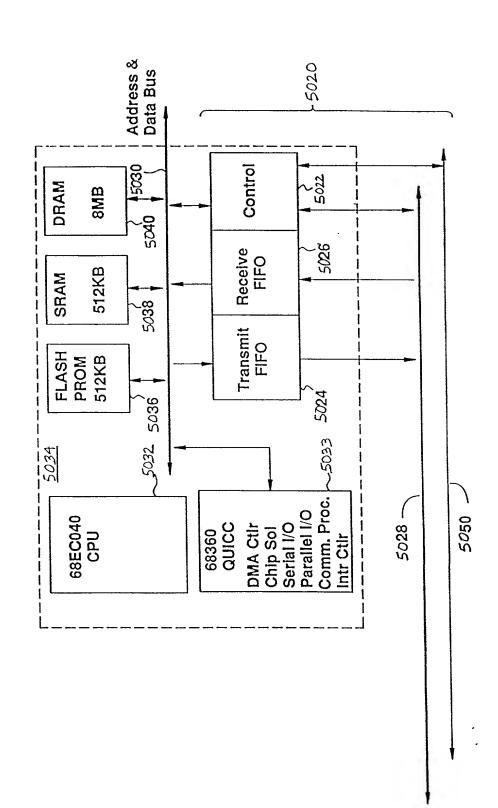


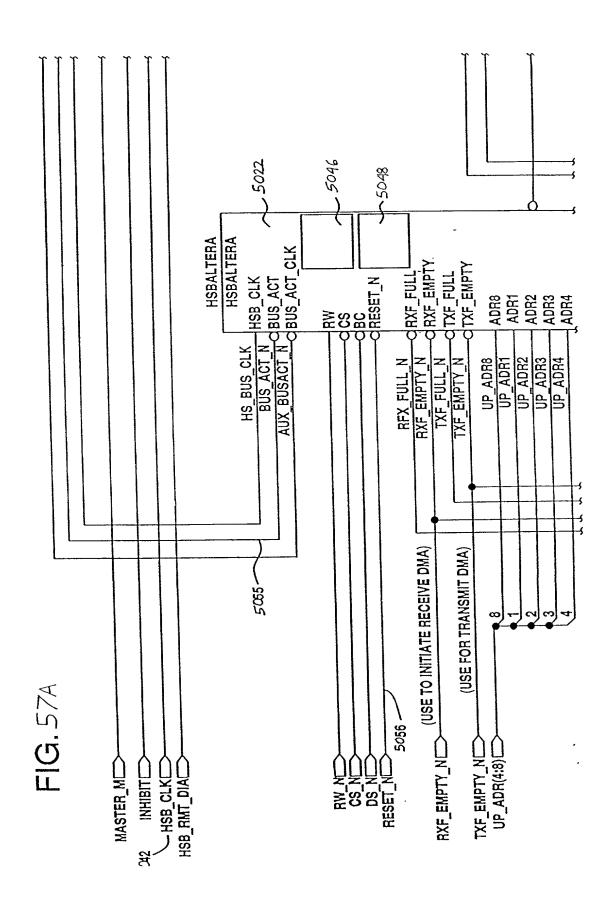
Comments	controller-type applications original IBM PC & compatibles accepts PC/XT cards enhanced PC/AT; auto-configure IBM PS/2; auto-configure IBM PS/2; auto-configure IBM PS/2; auto-configure IBM PS/2; auto-configure Intel; SUN-I and others data acqusition & control bus VAX 780, 8600 series; parity parity; 40MB/s for blk xfer, 20M otherwise parity; 40MB/s for blk xfer, 20M otherwise daisy-chained IACK; SUN-3
Connector b	H DEN ZER GREGER
Drivers	
IRQ Lines ^a	10E
Sync/Async	00004440000444
Multimaster?	110
MUXed data/adr?	11111111111
Block xfer?	111
Address width	16 20,24 20,24,32 24,(32) 22 22 22 20,24 9 32 16,32 32 16,32 32 32 32 32 32 32 32 32 32 32 32 32 3
Data width	8 8,16 8,16,32 8,16,(32) 16 24 8,16,24,32 8,16,24,32 8,16,24,32 8,16,24,32
RAW bandwidth (Mbyte/s)	3.3 3.3 3.3 3.3 40 40 160
SUS	STD bus PC/XT PC/AT EISA MicroChannel Q - bus Multibus I CAMAC VAX BI NuBus NuBus VME Futurebus Fastbus

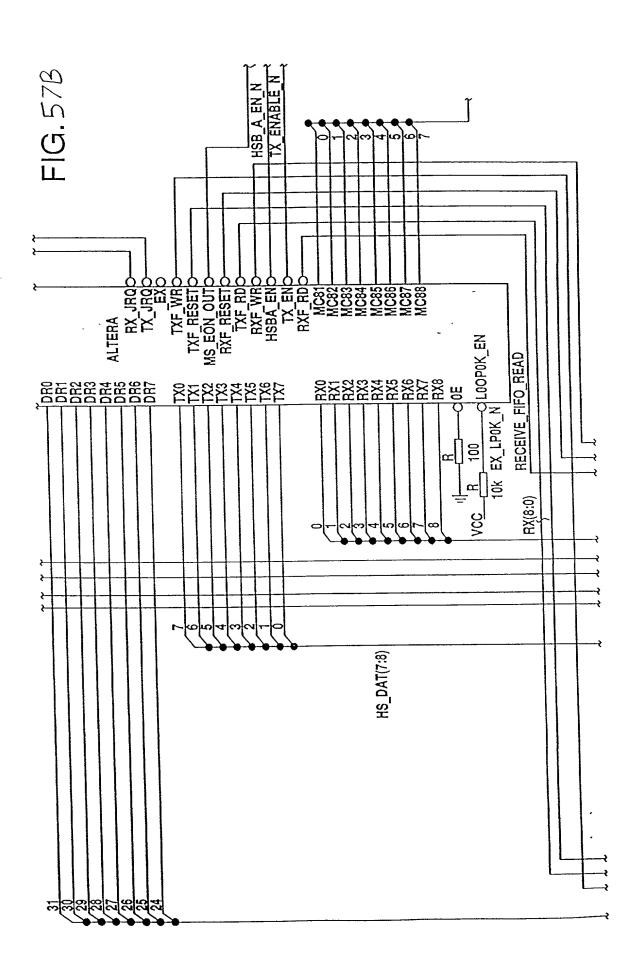
(a)E-edge-sensitive;L-LAM ("look at me");M-"int" via bus mastership; P-programmable edge-or level-sensitive interrupts.

(b) CE-card-edge; DIN-2-part "Eurocard" 96-pin connector; H-high density 2-part conn. (c) almost. (d) National Semi special.

FIG. 56





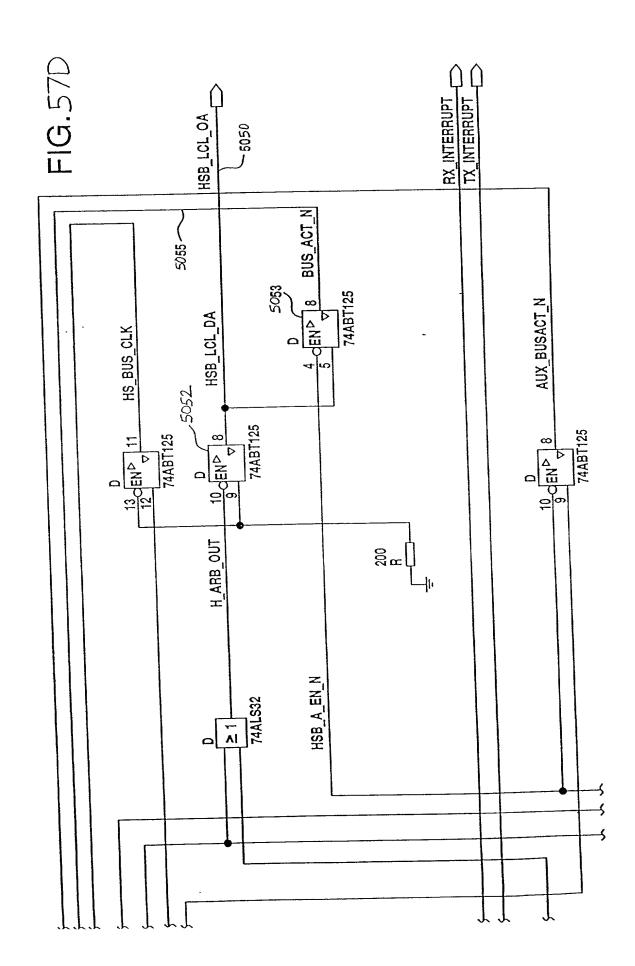


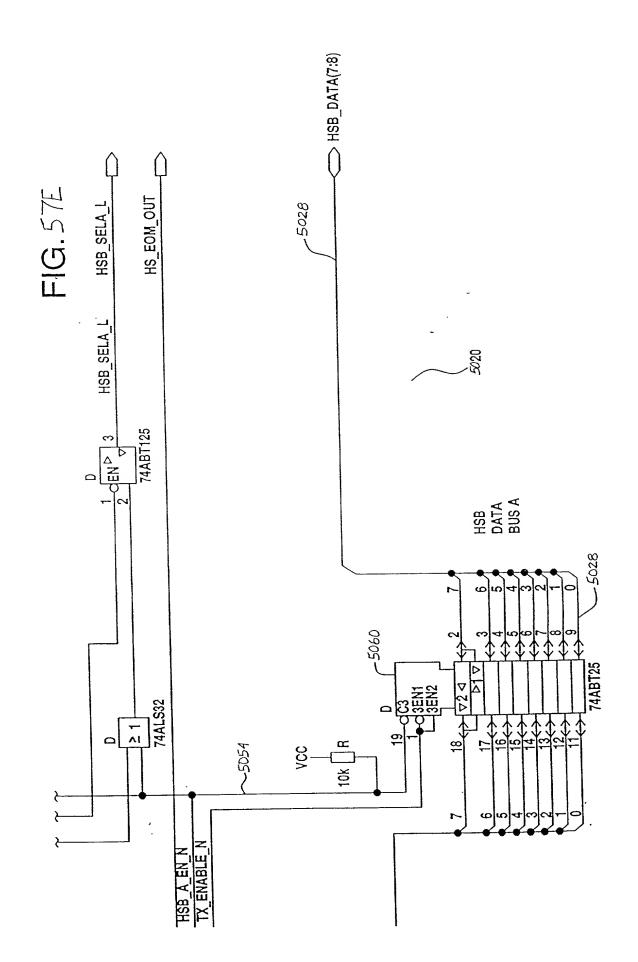
S S FULL 0 3 EMPTY 24 EMPTY 24 EXPANSION/H-FULL 0 23 N C FIFO1KX9_25 5026 FIFO1KX9

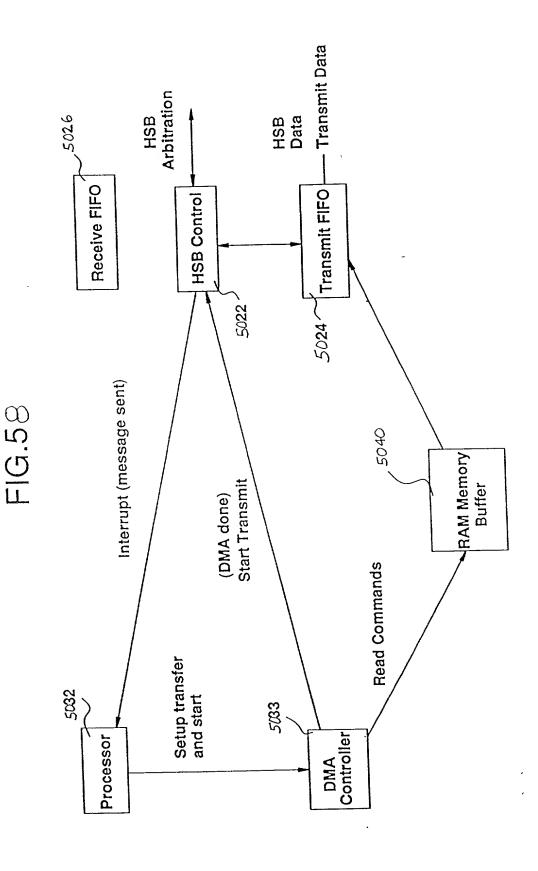
25 RESET
18 READ
2 WRITE
1.26 UAD/RETRANSMIT D 8888 3 တက RECEIVE FIFO FRX RT 10K 1,23 N.C. 5024 EXPANSION/H-FULL CLOAD/RETRANSMIT TRANSMIT FIFO 25 RESET 18 READ 2 WRITE L 26 L OAD BETRANG FIF01KX9_25 50000 700 ব F 4 \$ 3_DAT(31:0) 5330

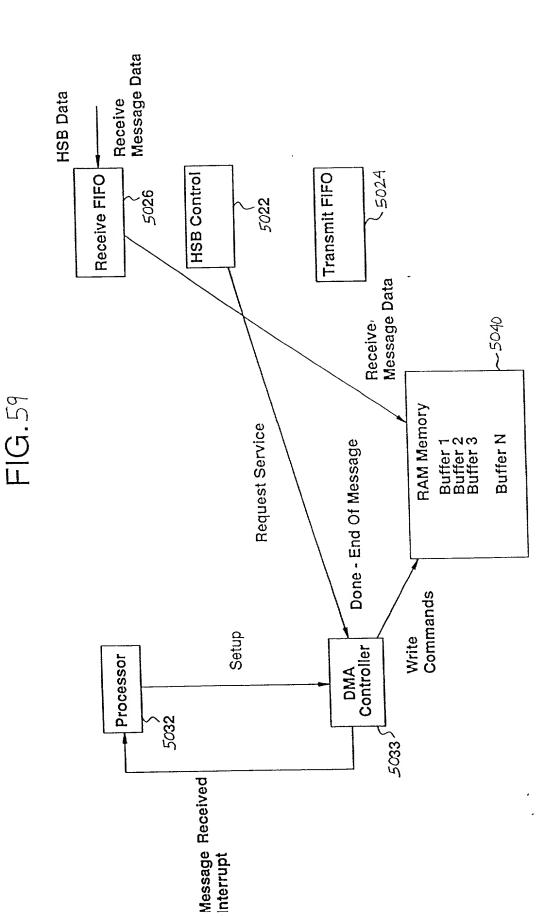
222222

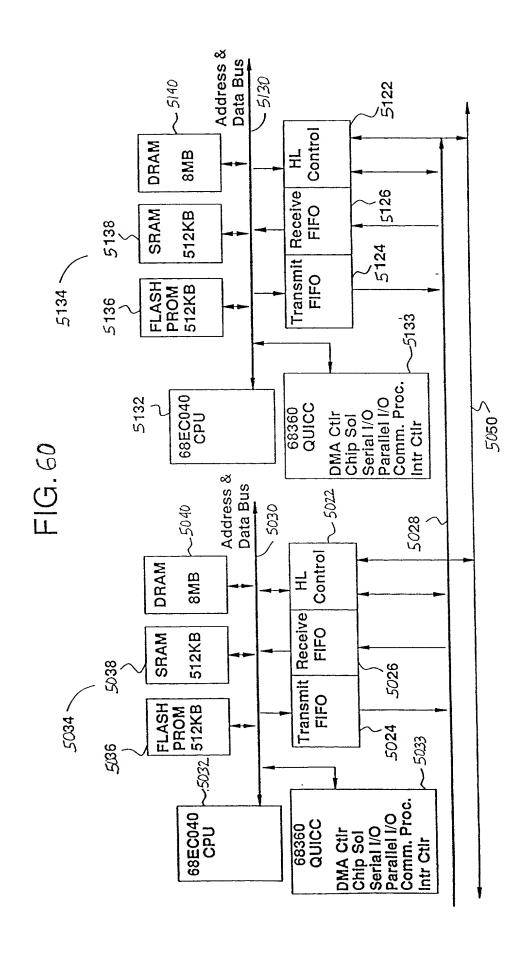
FIG. 57C











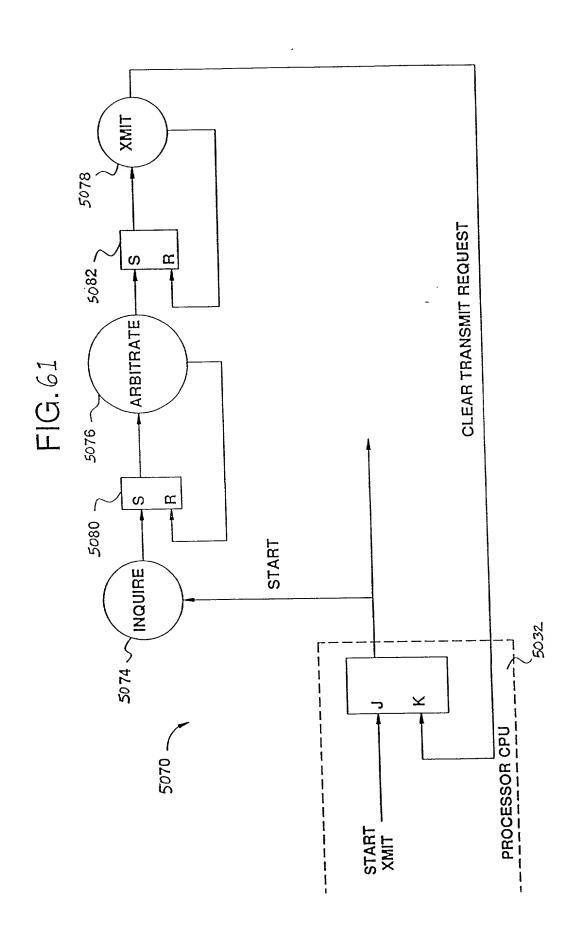


FIG. 62

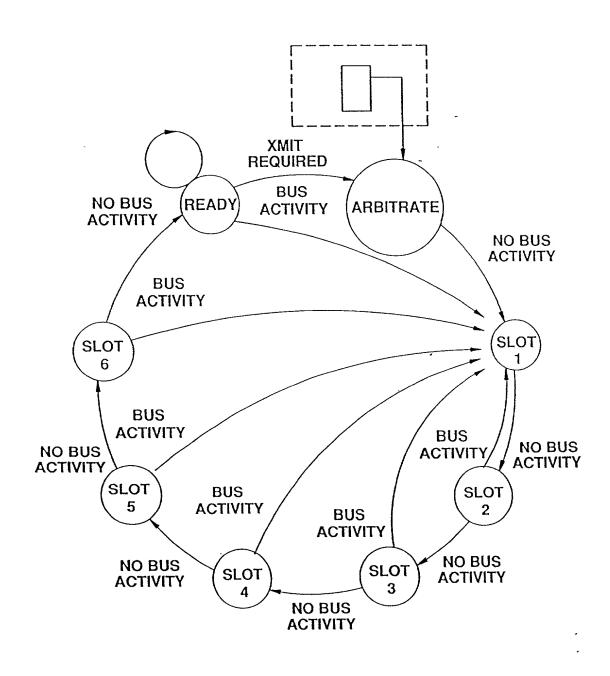


FIG. 63

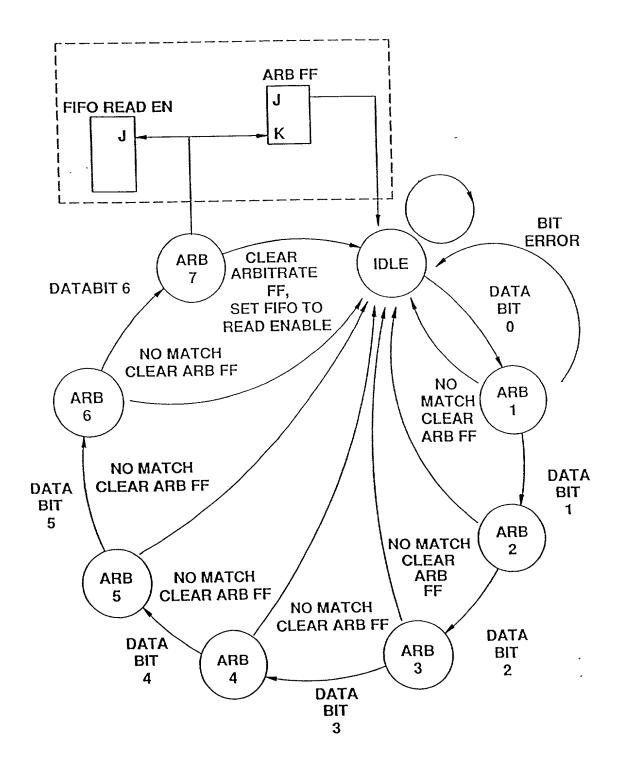


FIG.64

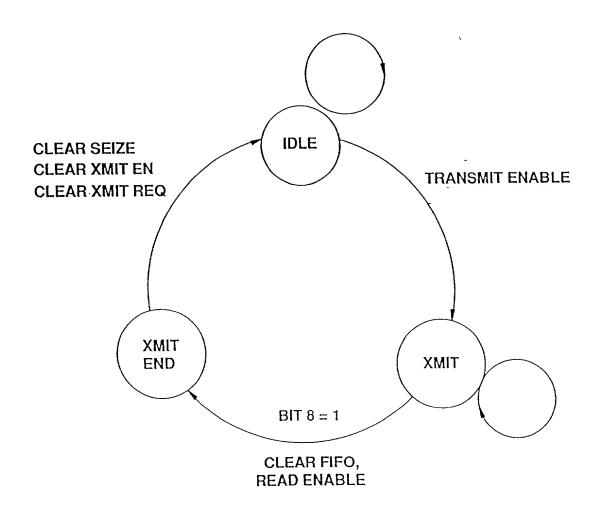


FIG. 65

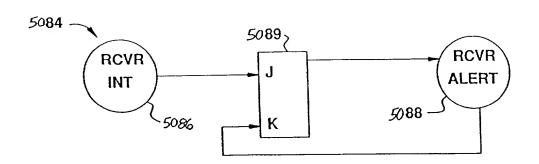


FIG.66

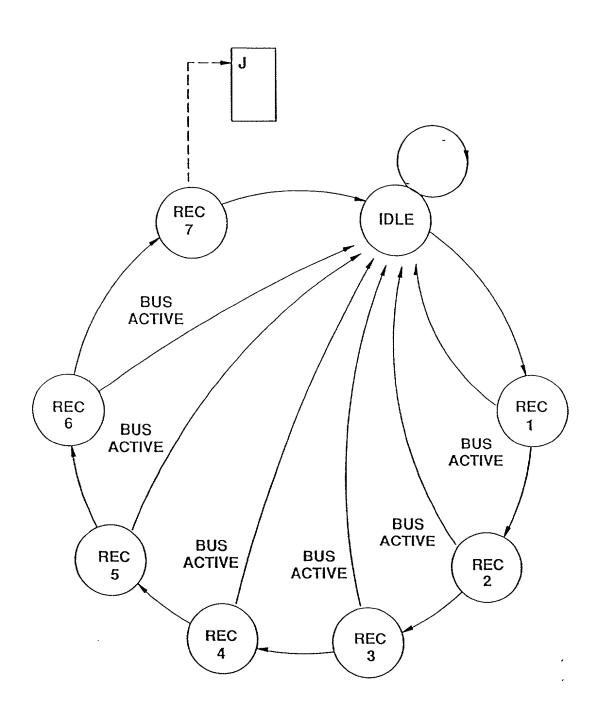
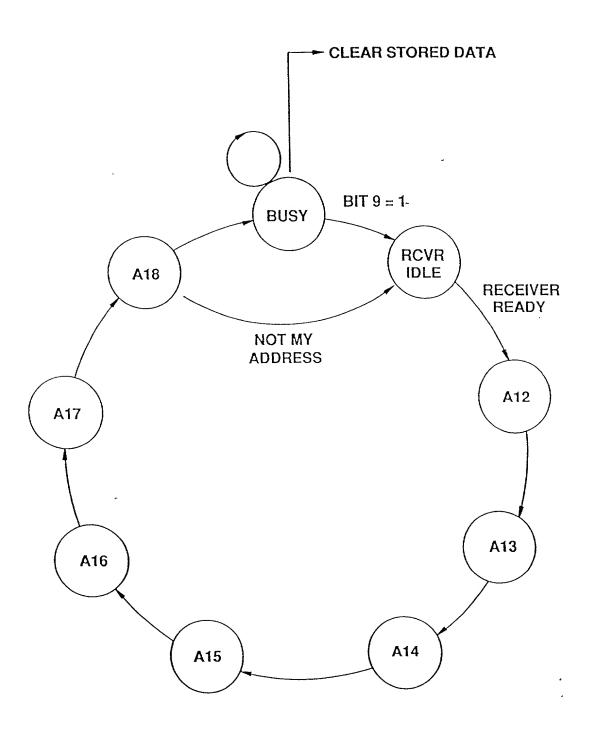


FIG. 67



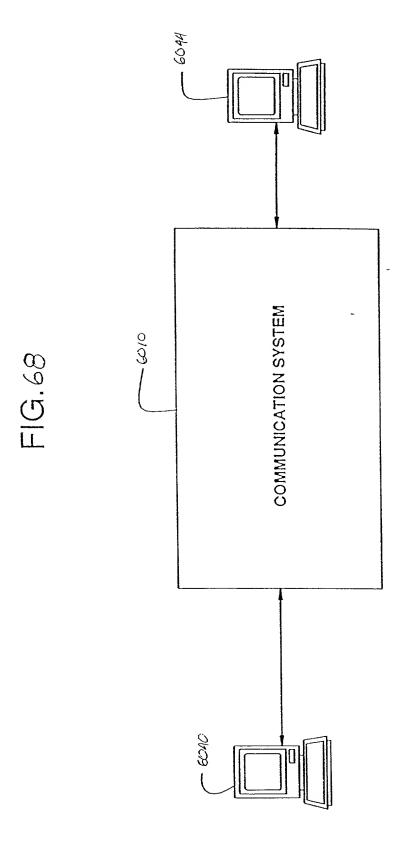
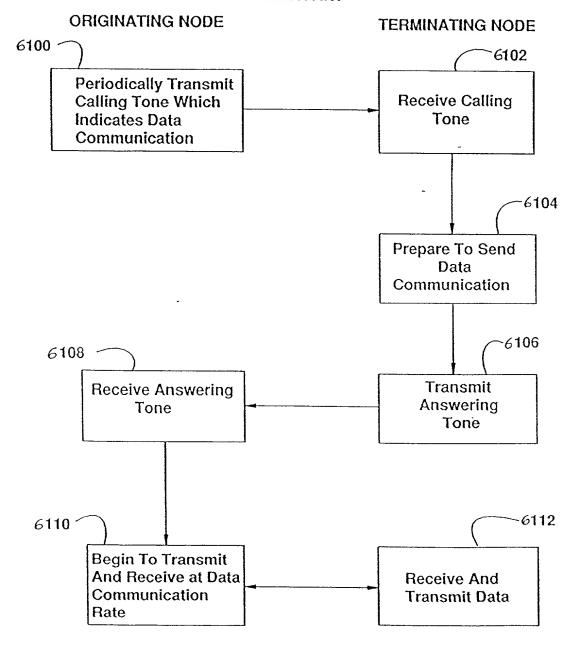


FIG. 69 PRIOR ART



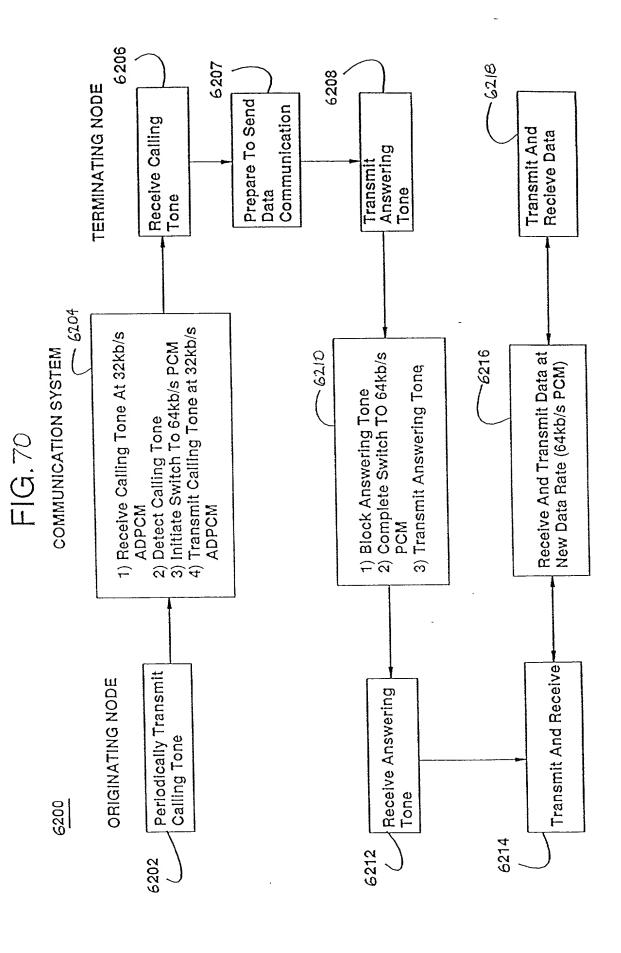
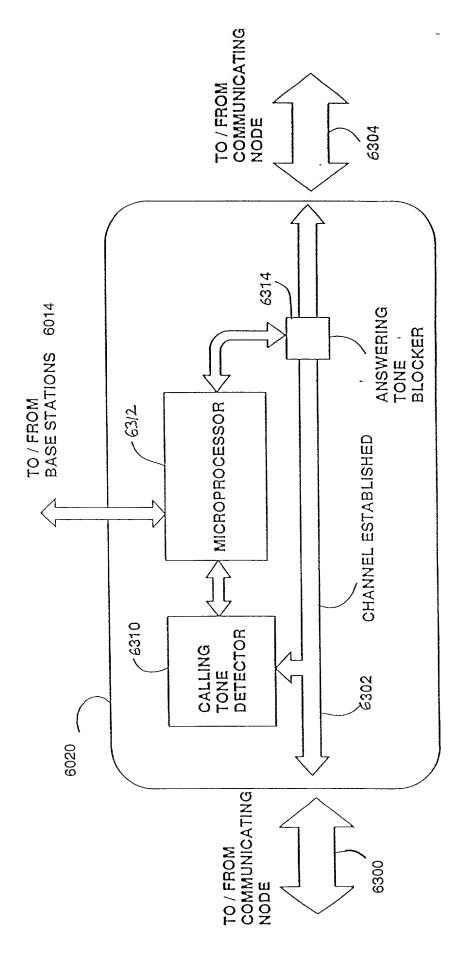
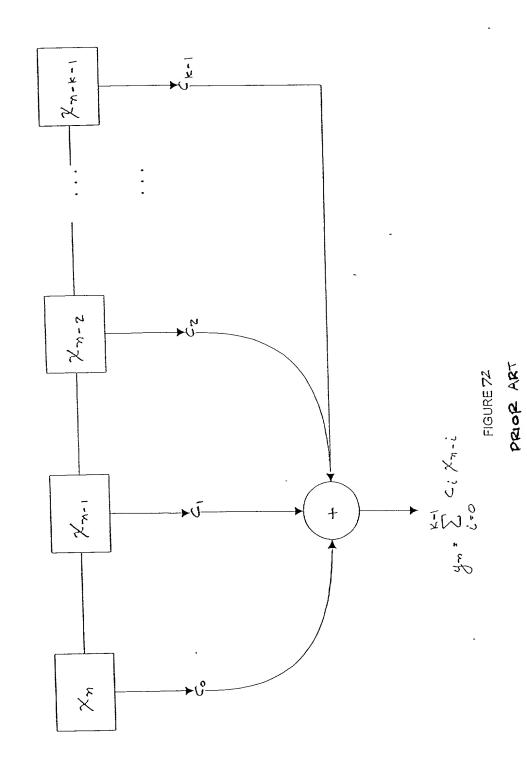
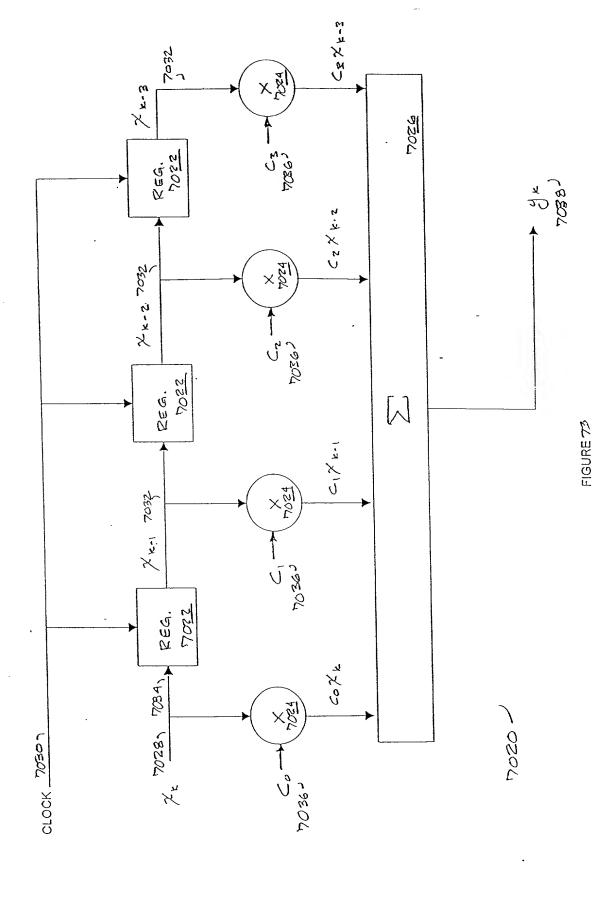


FIG. 71



0201





PEDD APT

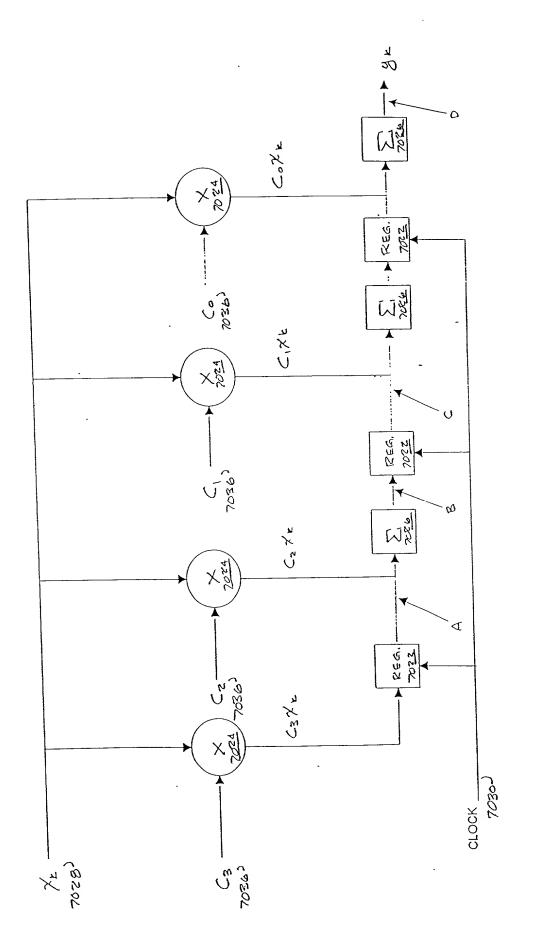
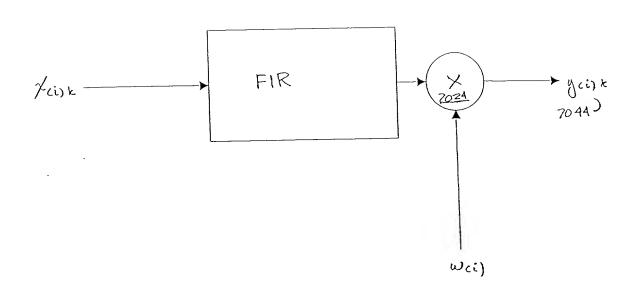
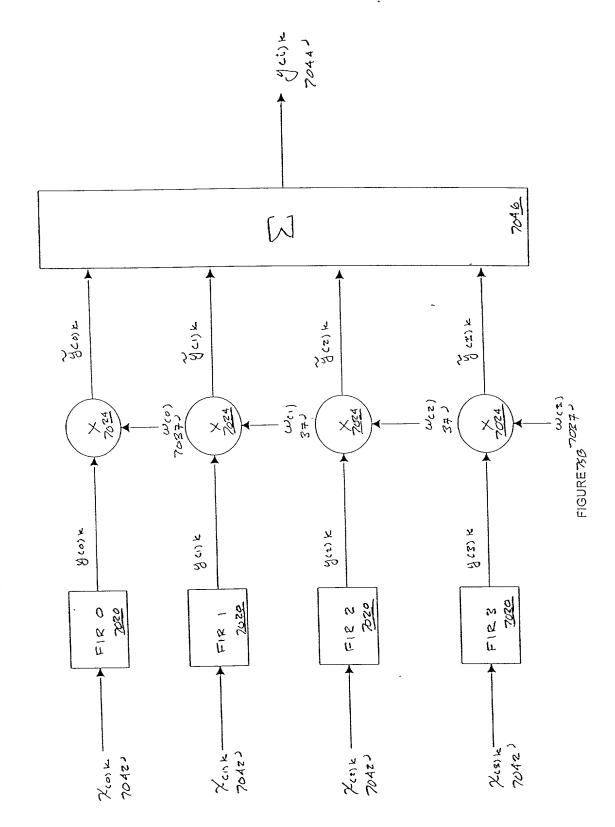
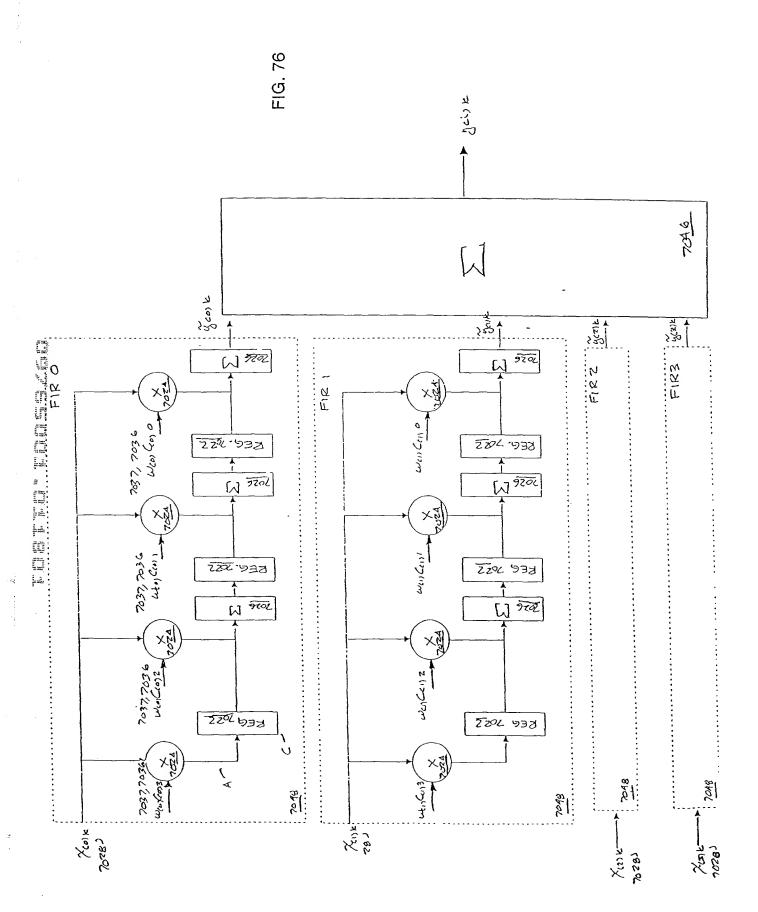


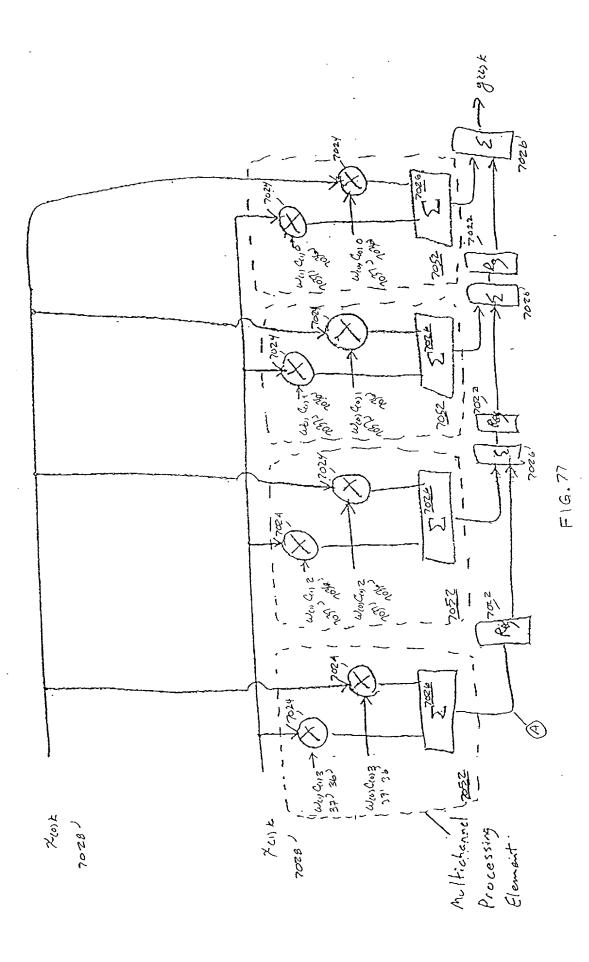
FIGURE 74
PROR ART

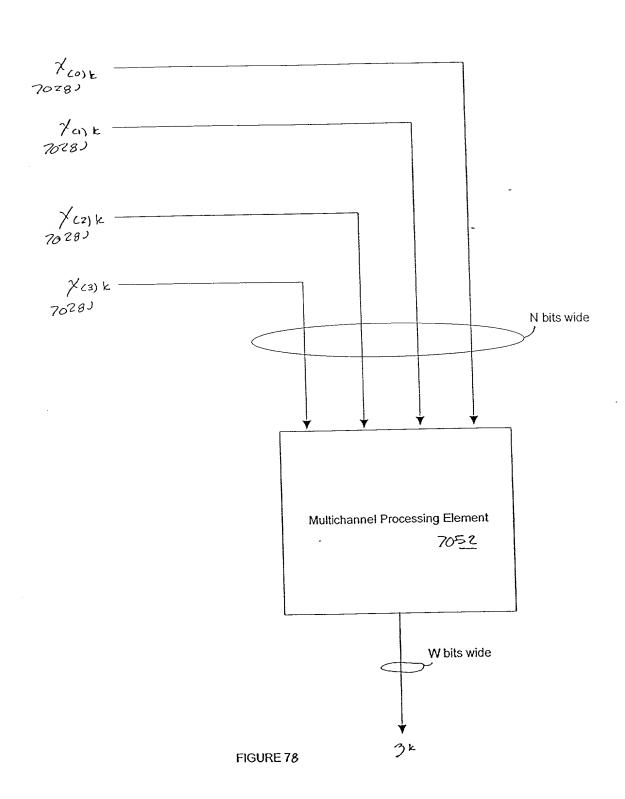
7040-











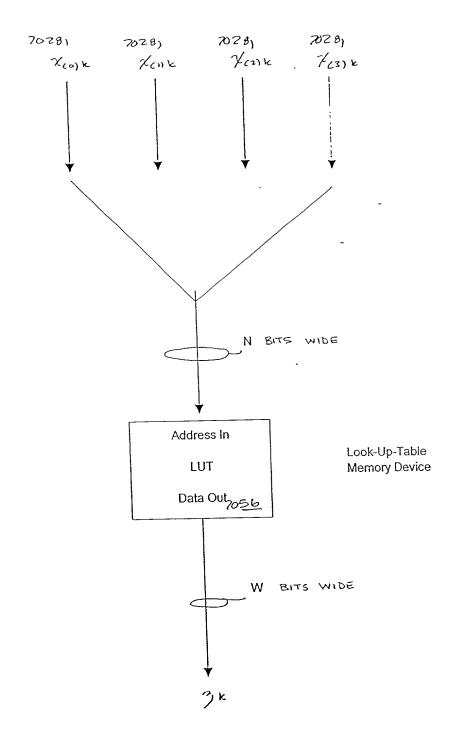


FIGURE 794

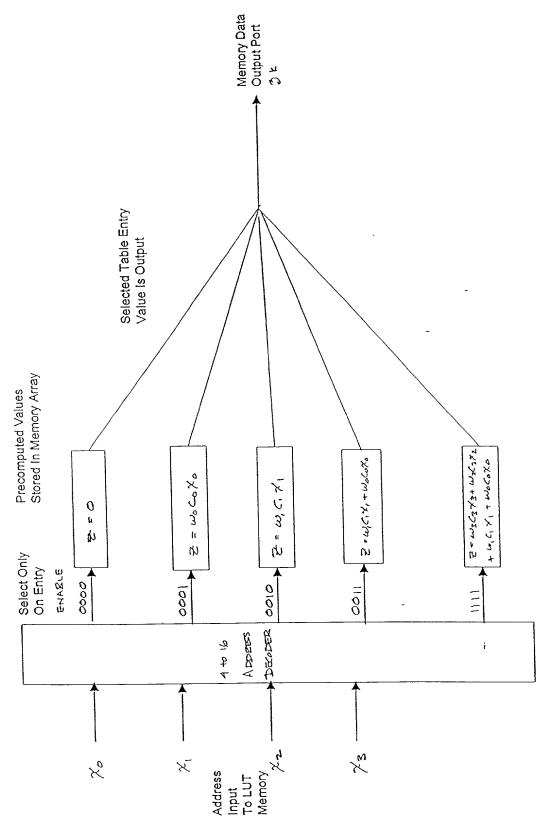


FIGURE 79B

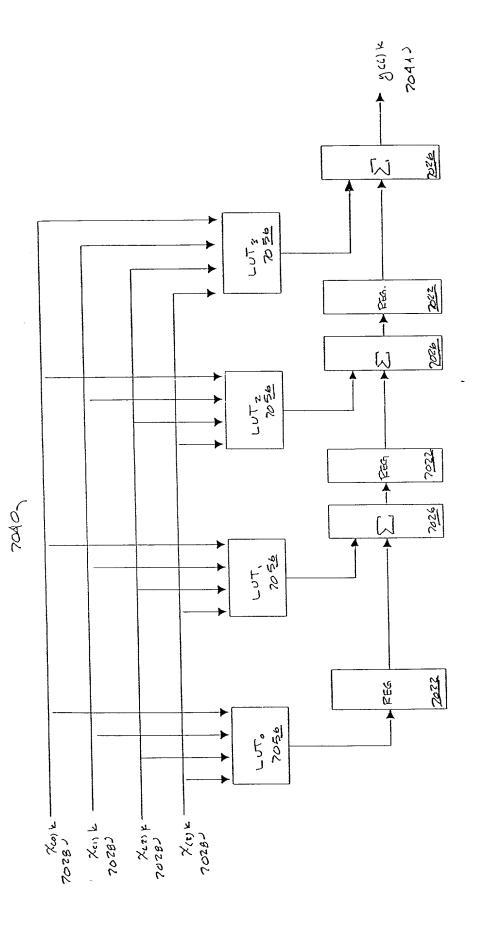


FIGURE 80